

Improved lower bounds for QAC⁰

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UC Berkeley

Based on joint work with:



Avishay Tal

UC Berkeley



Francisca Vasconcelos

UC Berkeley

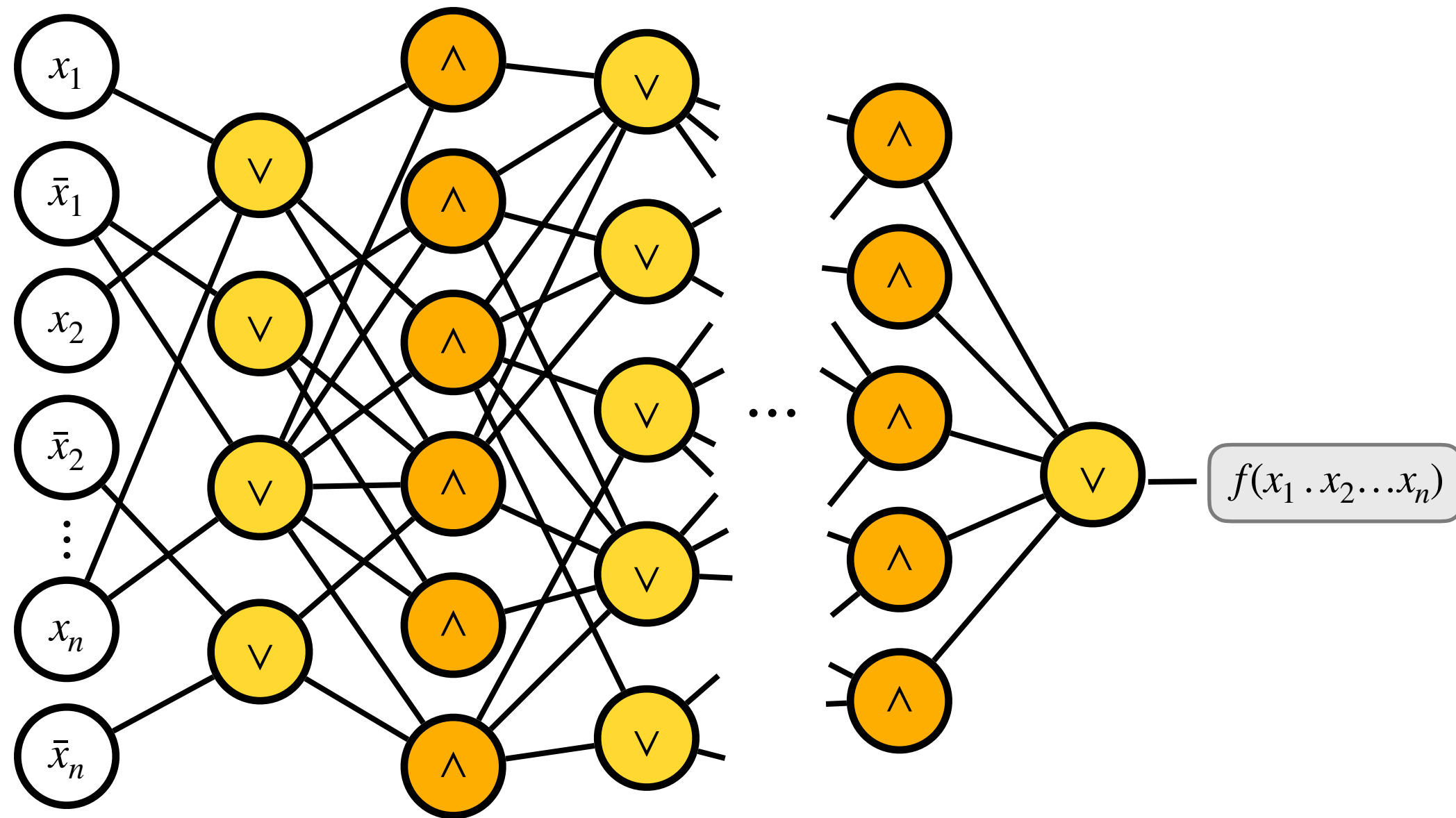


John Wright

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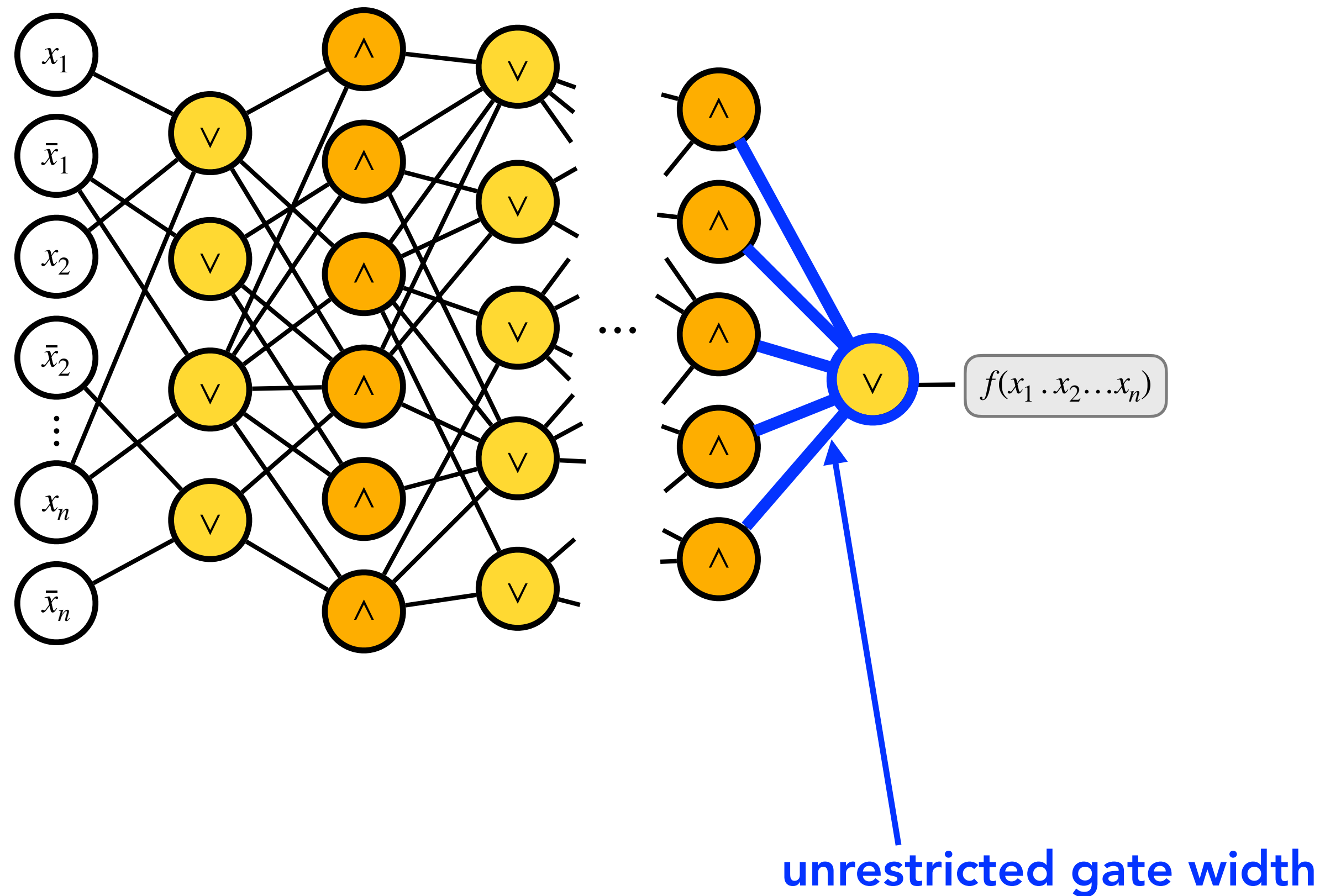
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AC^0 : AND + NOT



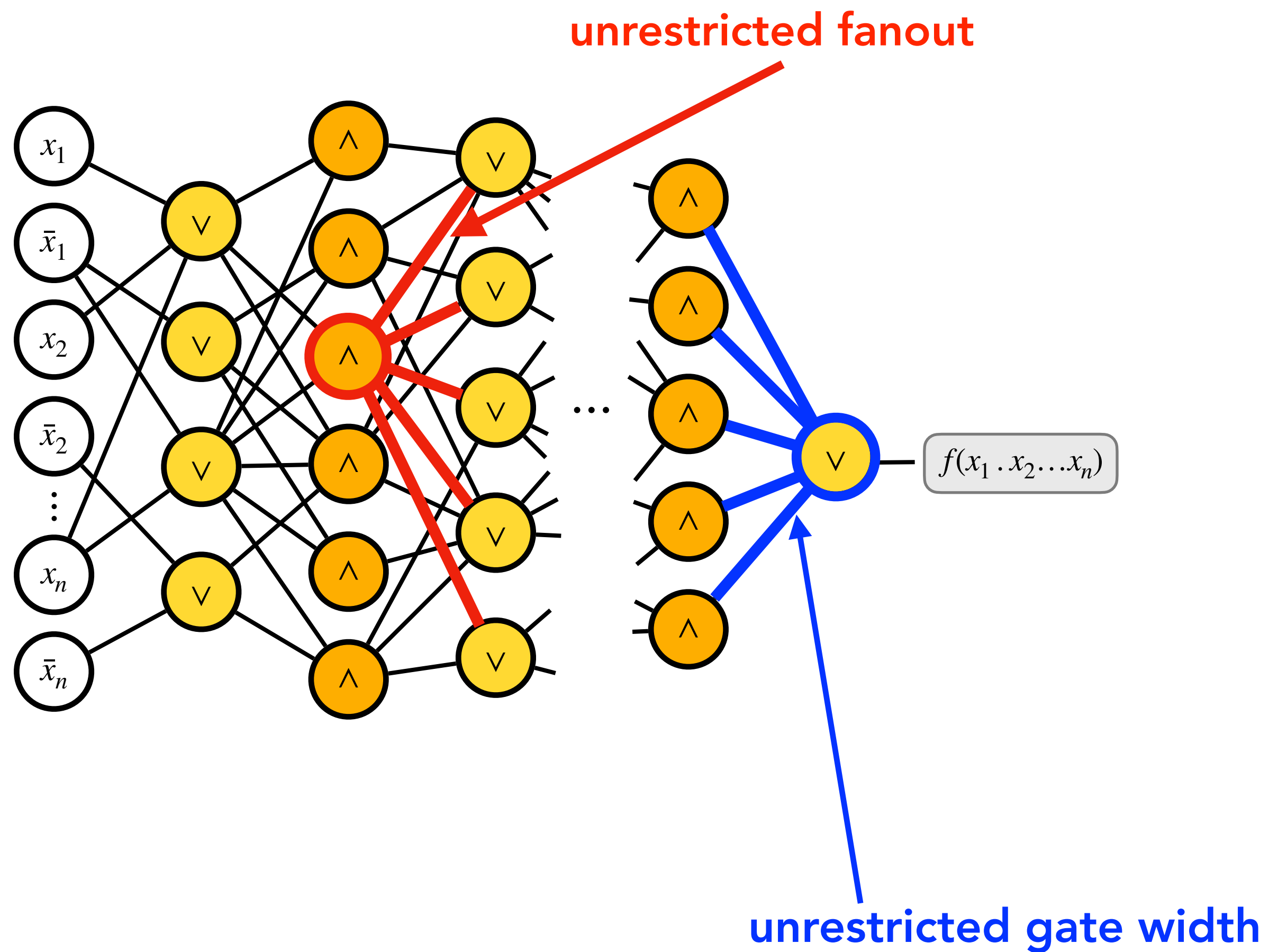
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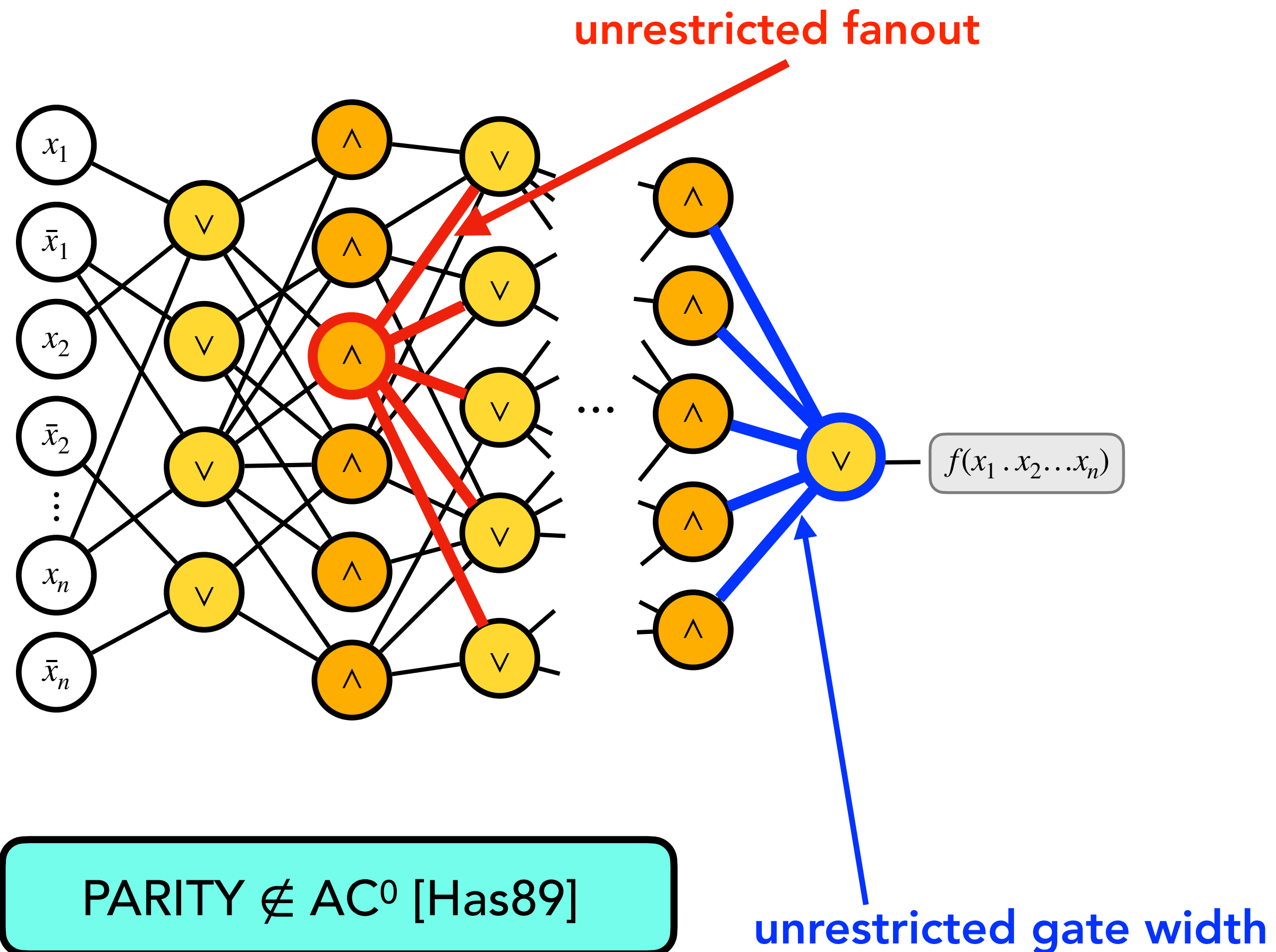
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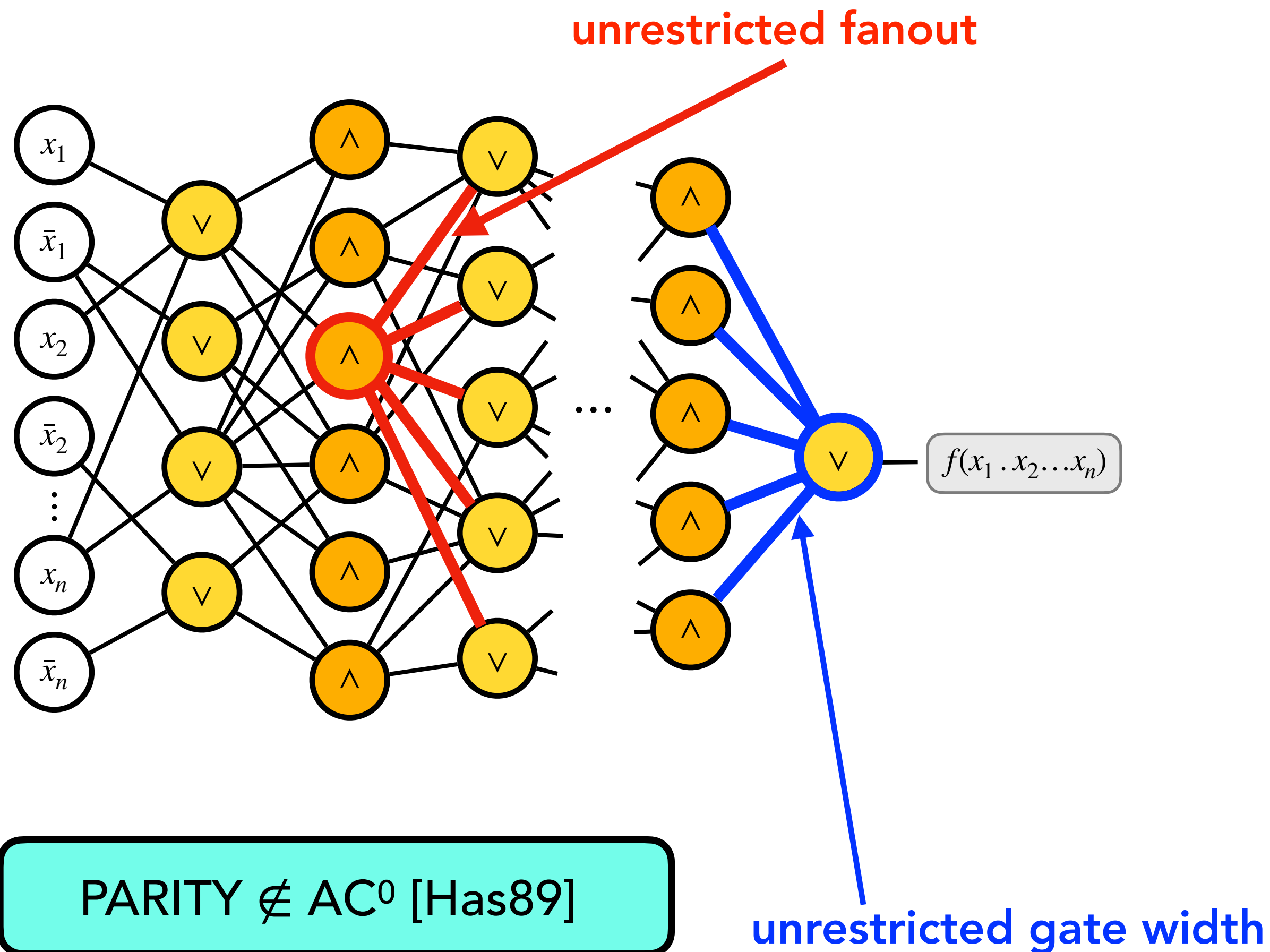
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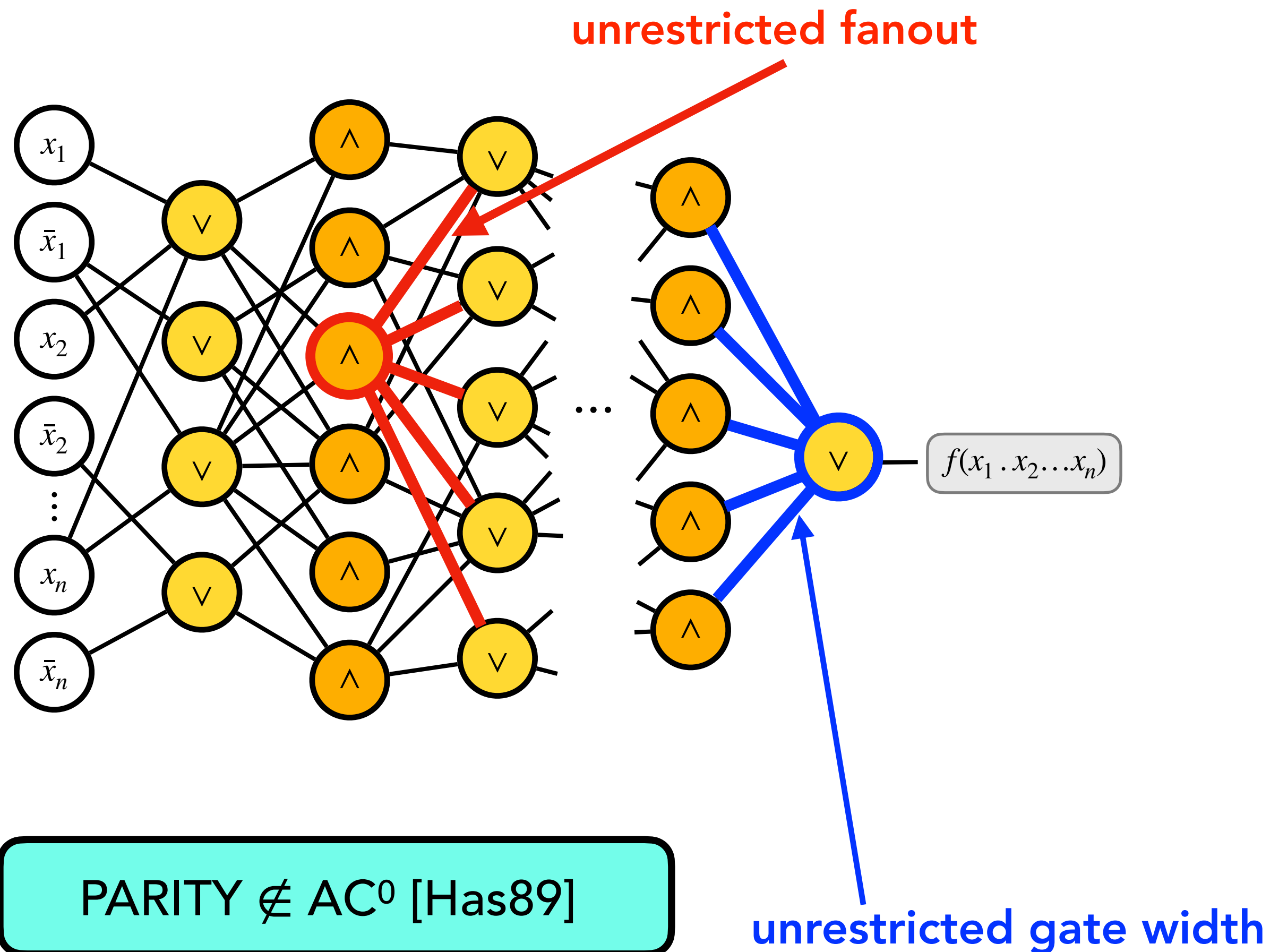
QAC^0 : Reversible-AND



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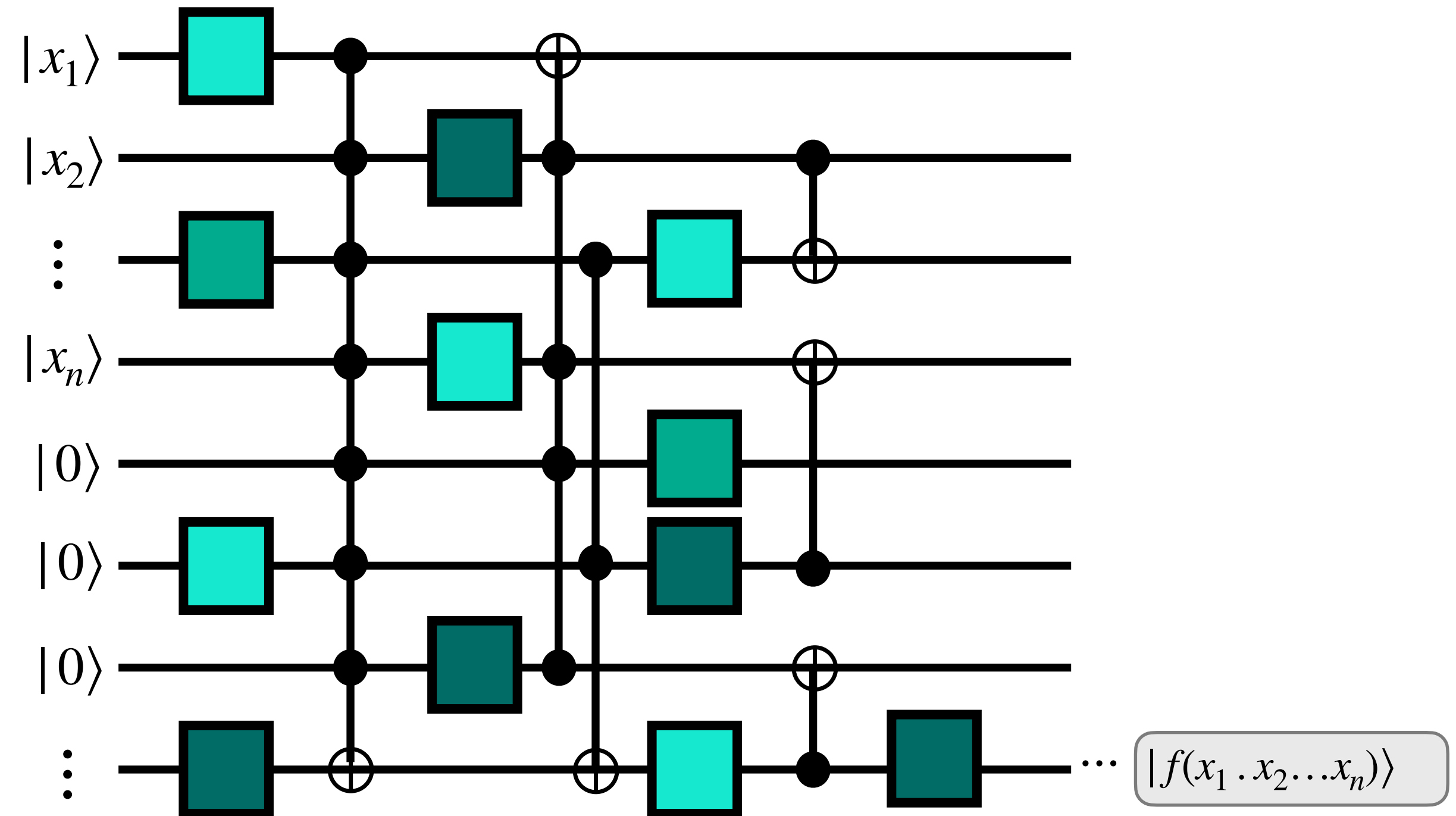
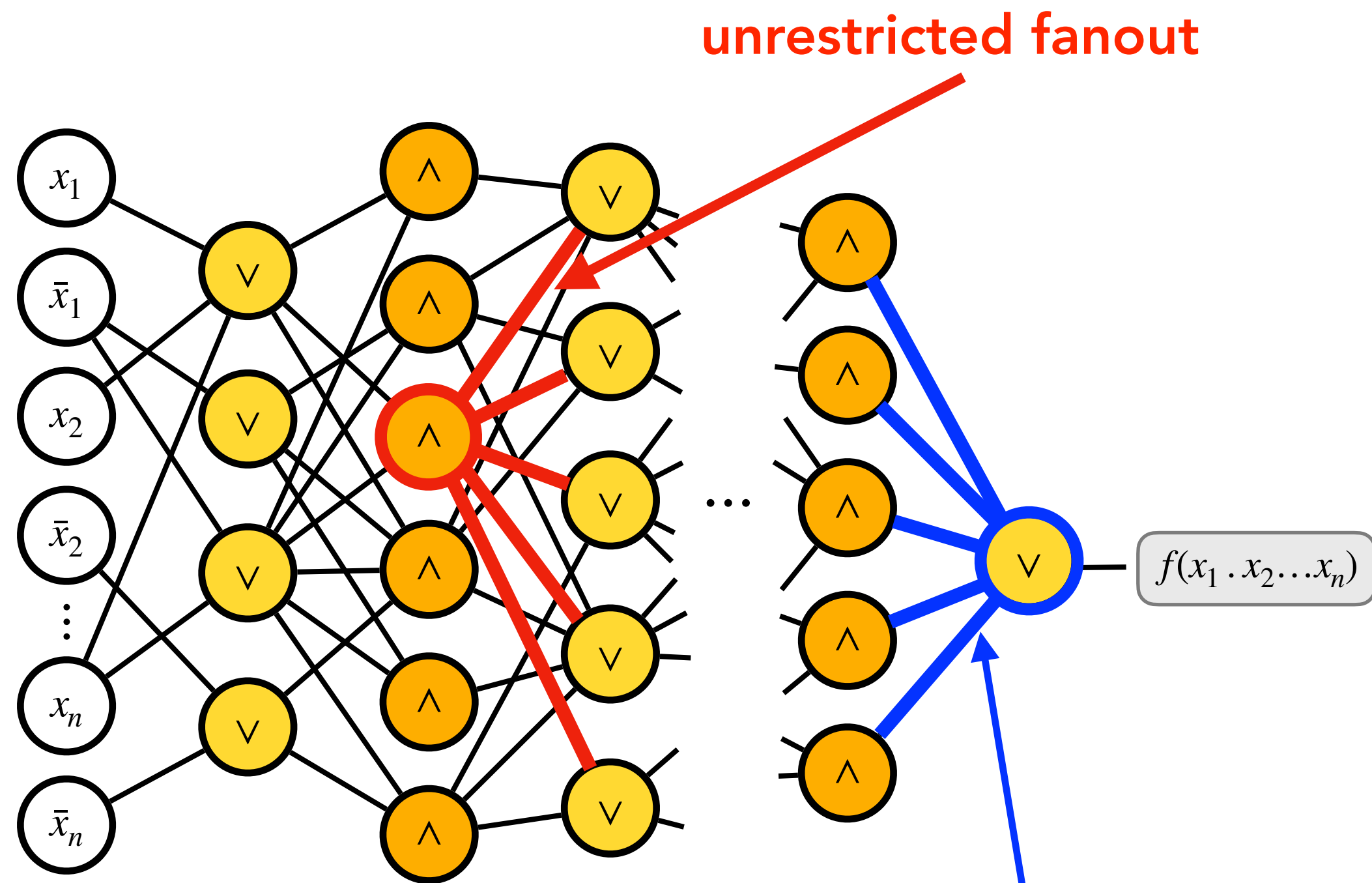
QAC^0 : Reversible-AND + Single-qubit Unitaries



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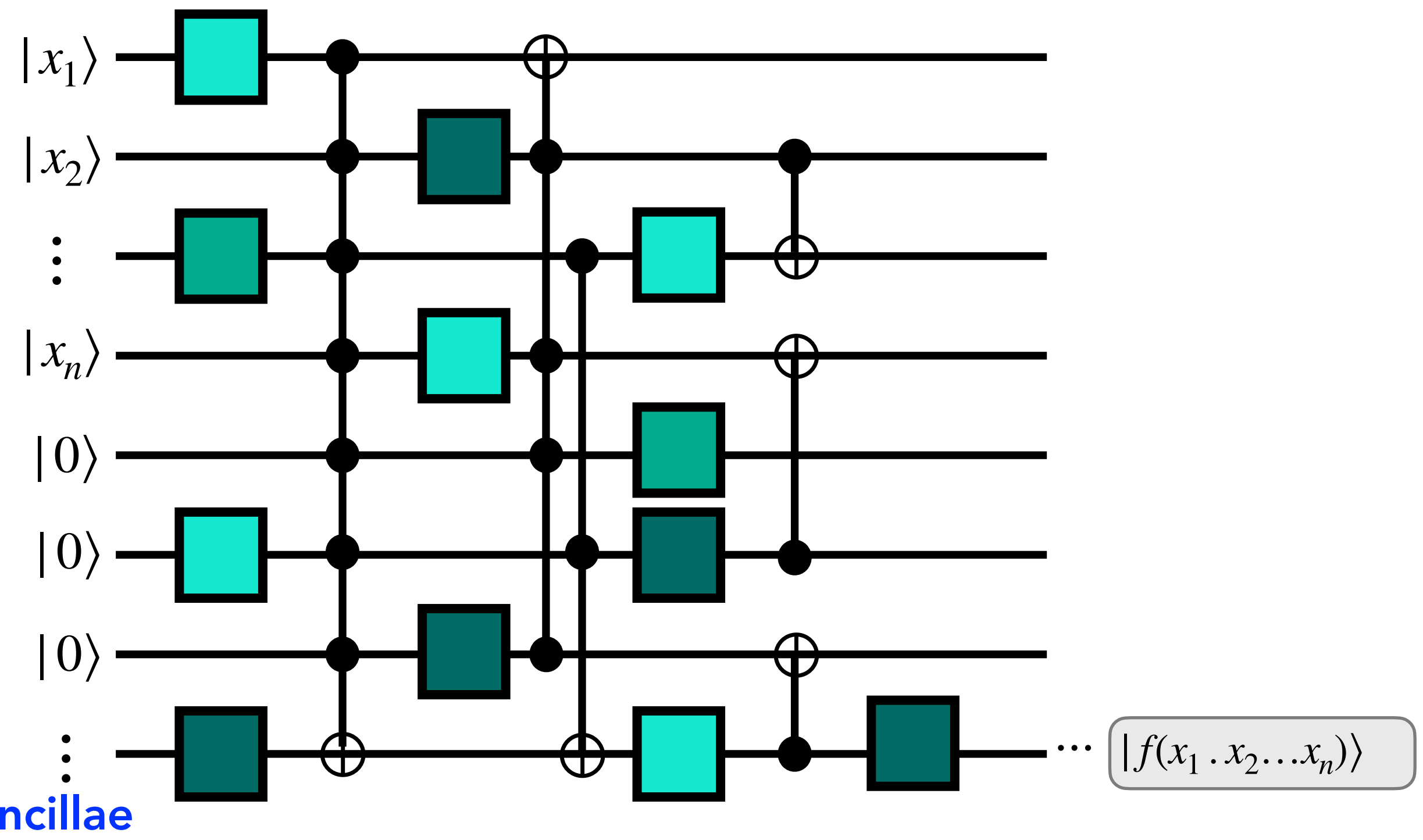
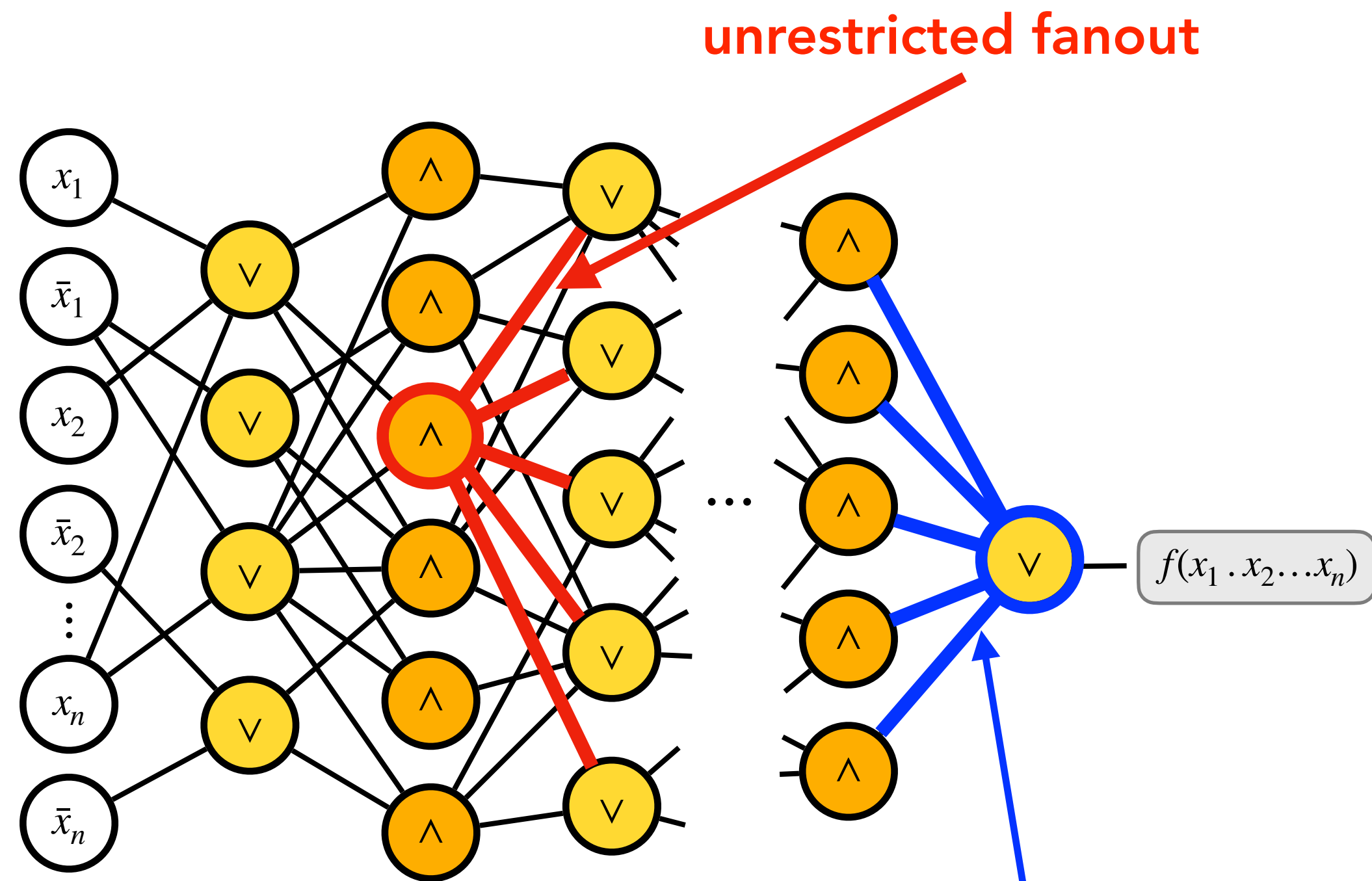
PARITY $\notin \text{AC}^0$ [Has89]

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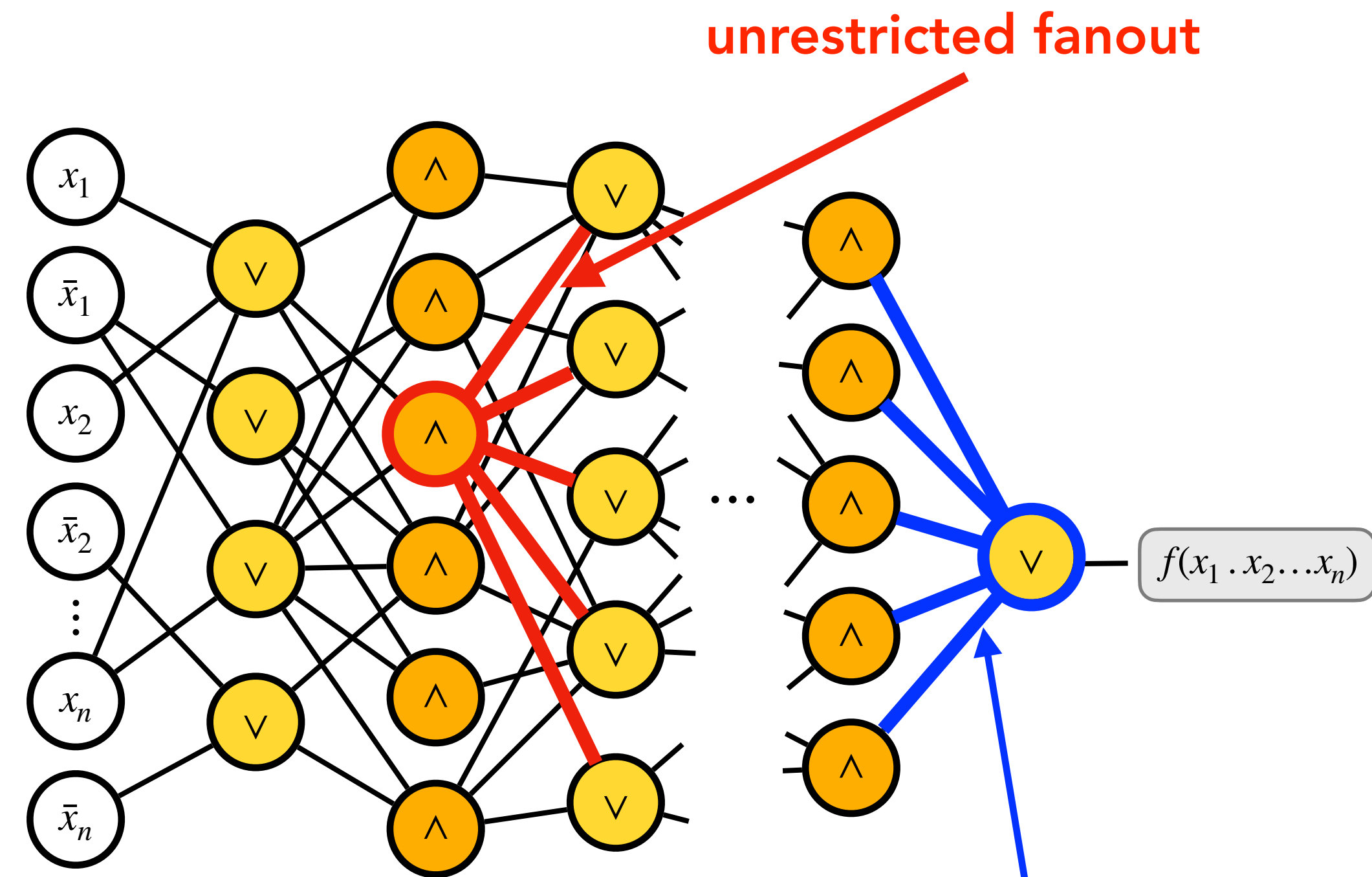
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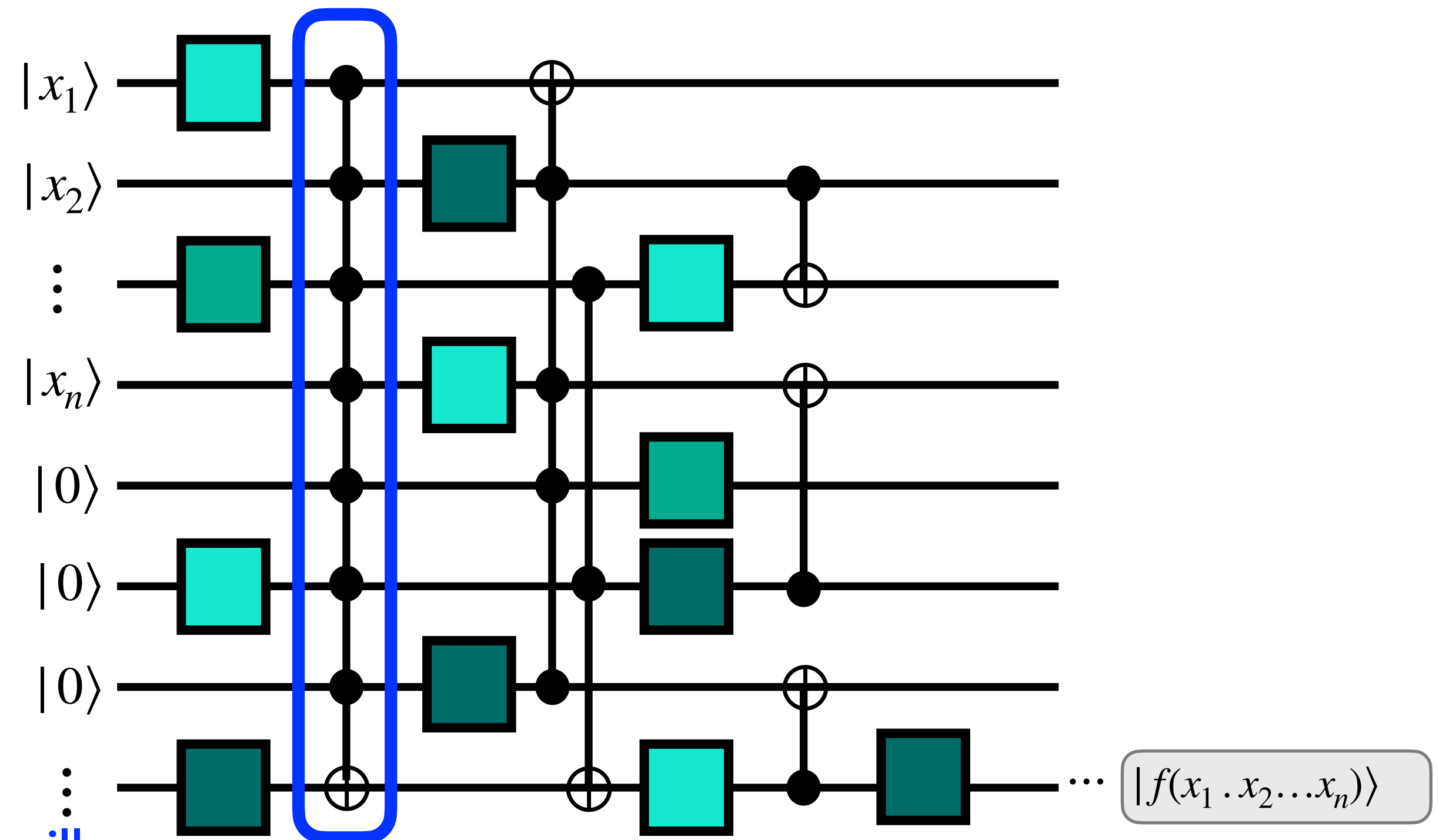
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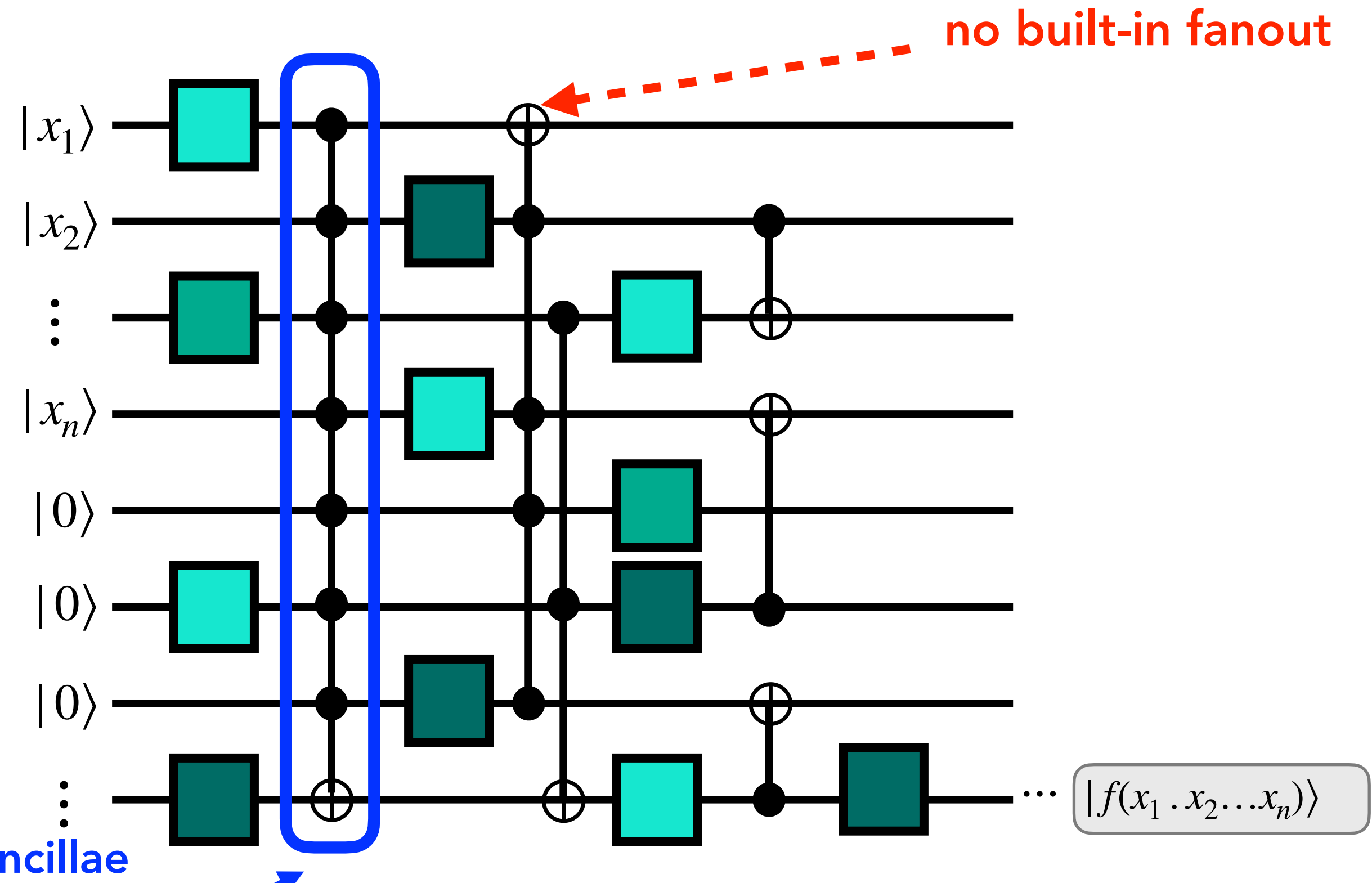
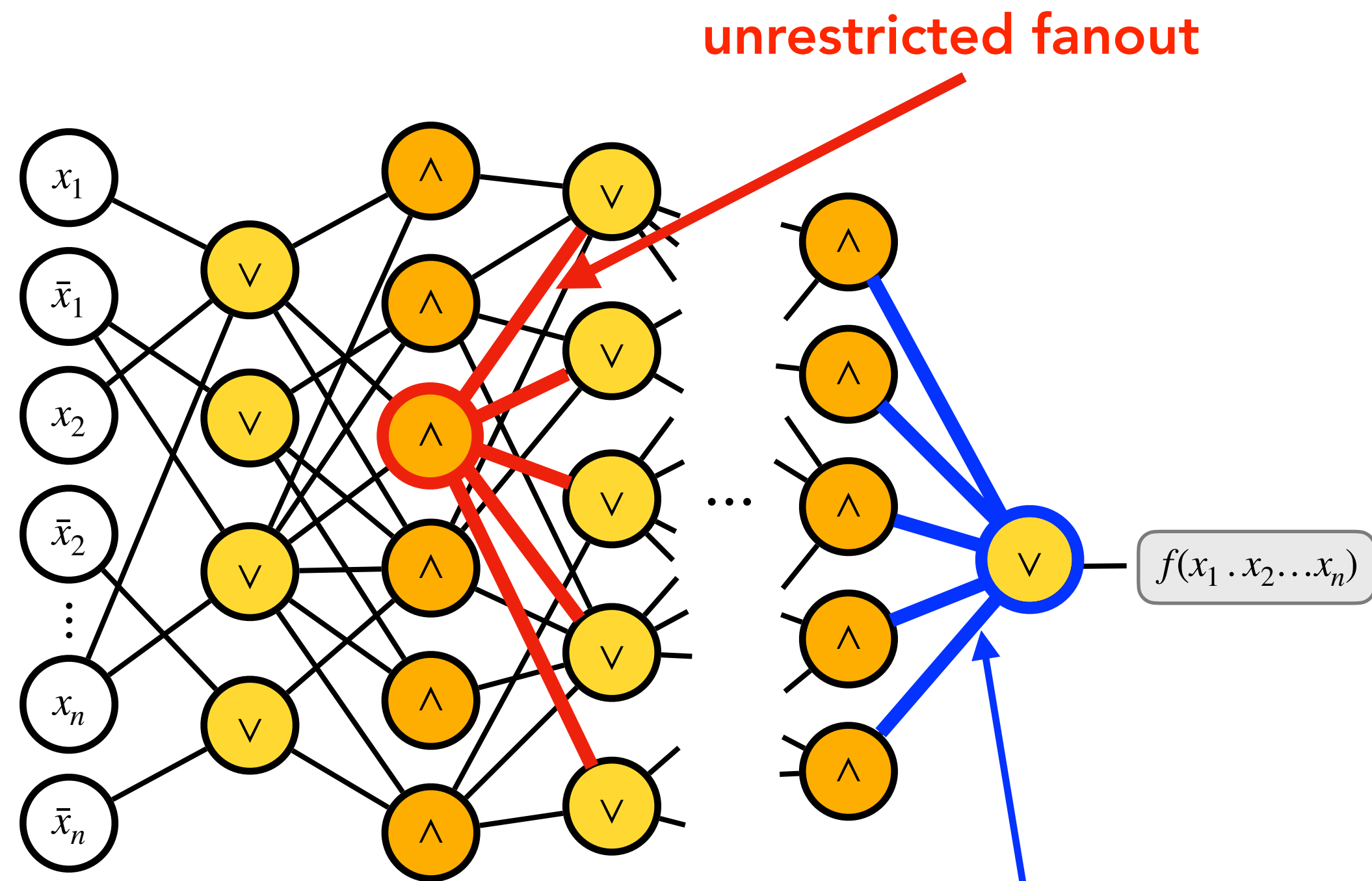


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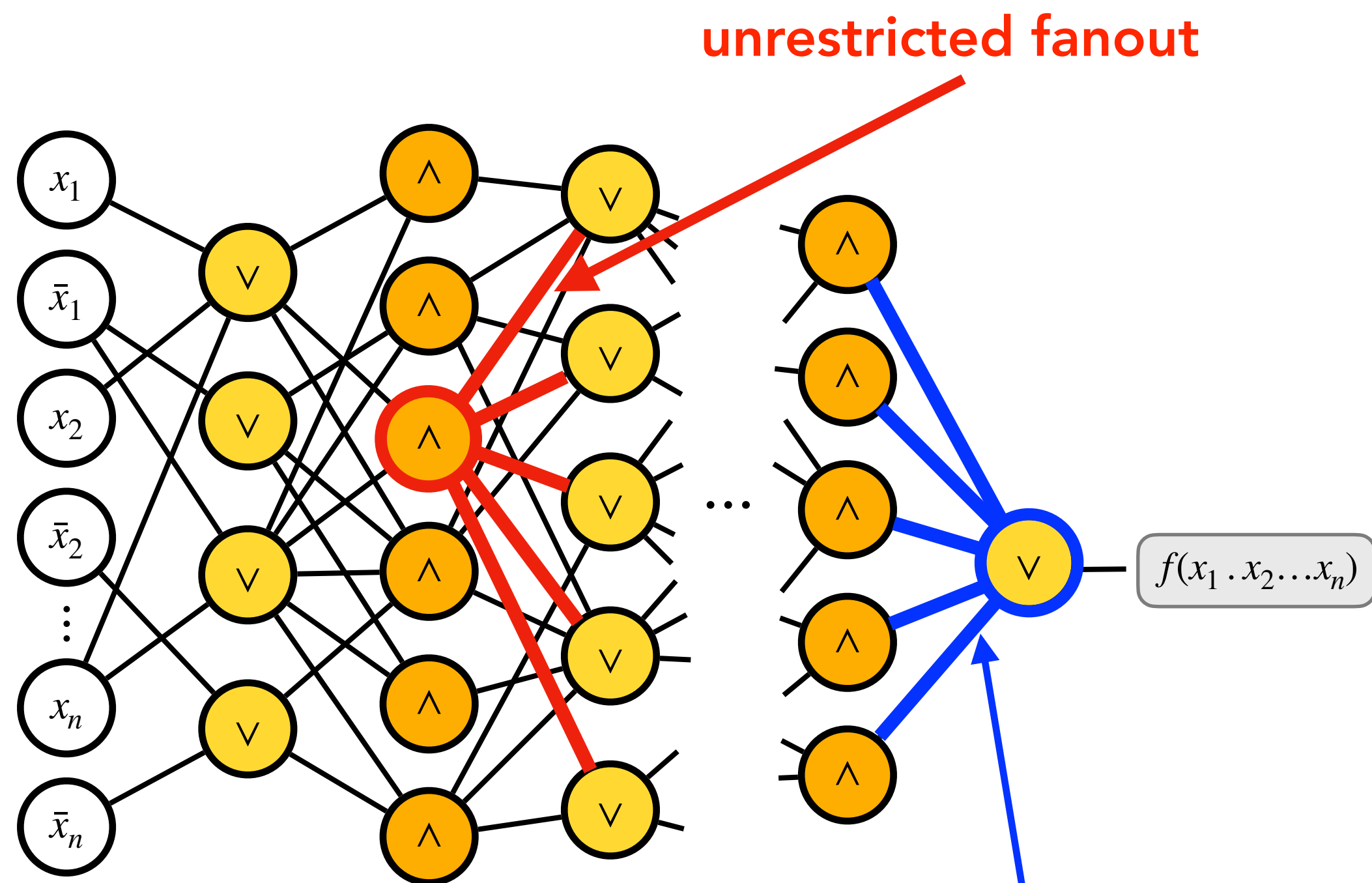
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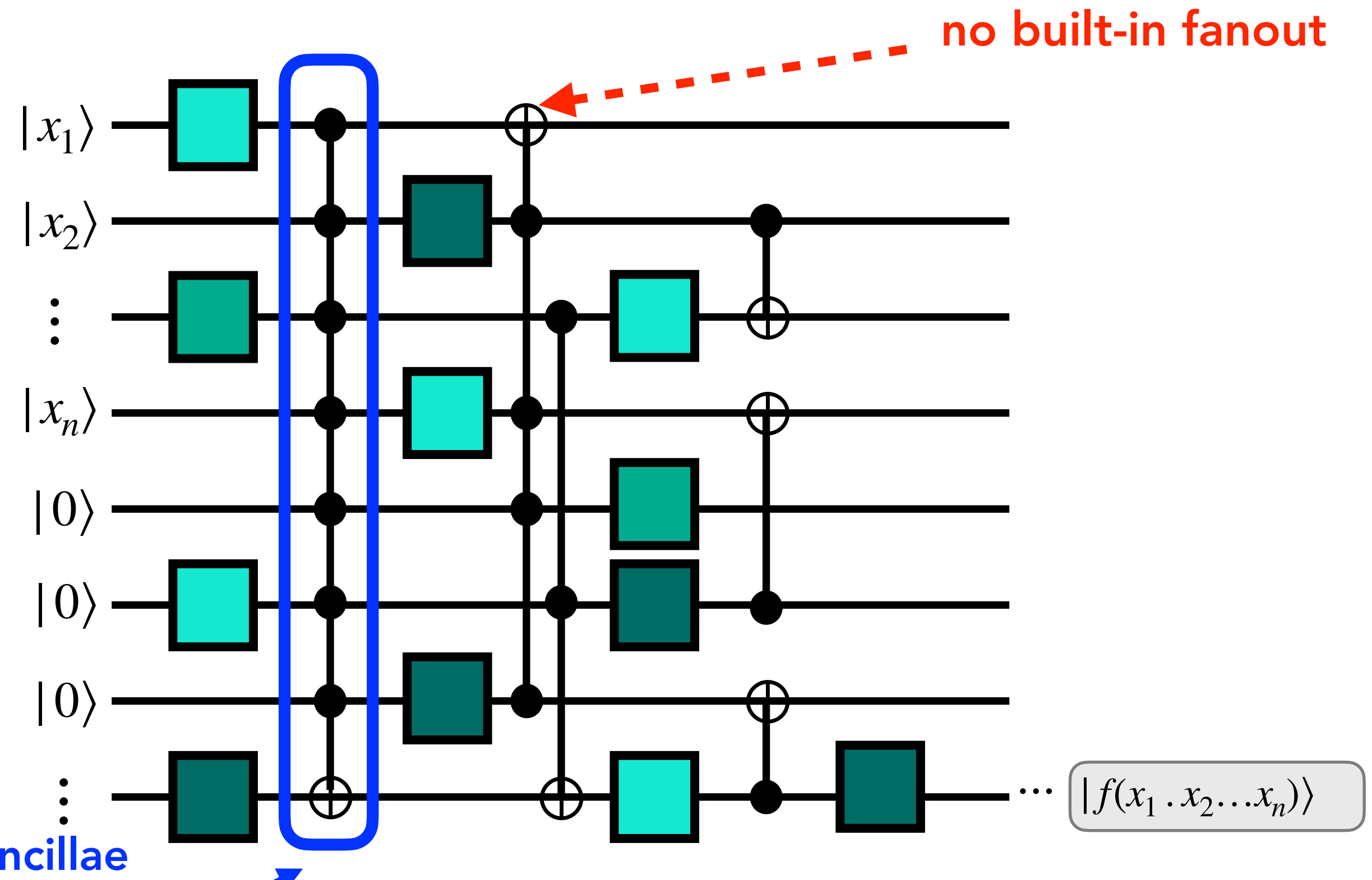
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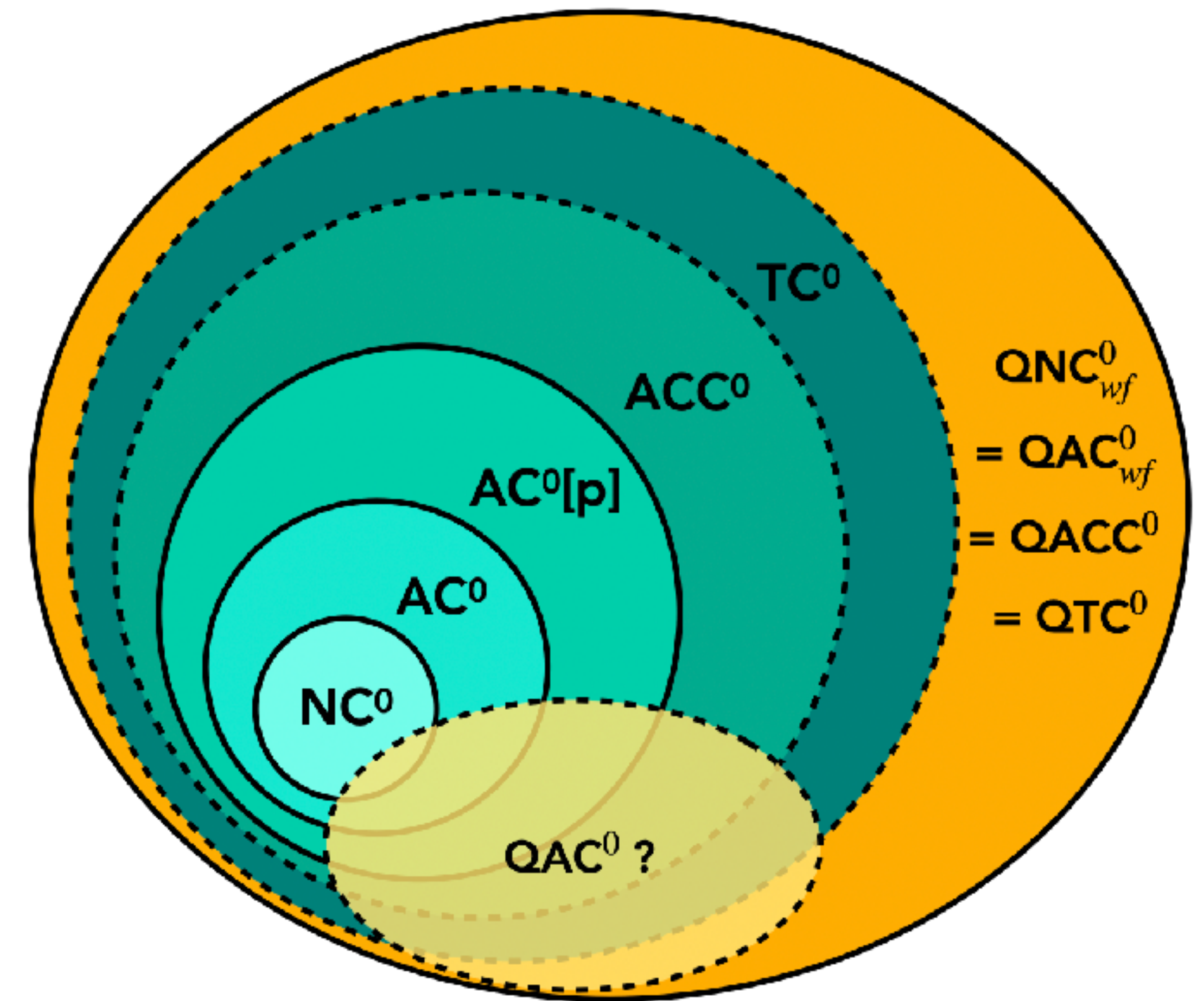
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PARITY ?? QAC^0 [Moo99]

Motivation: Why QAC^0 ?

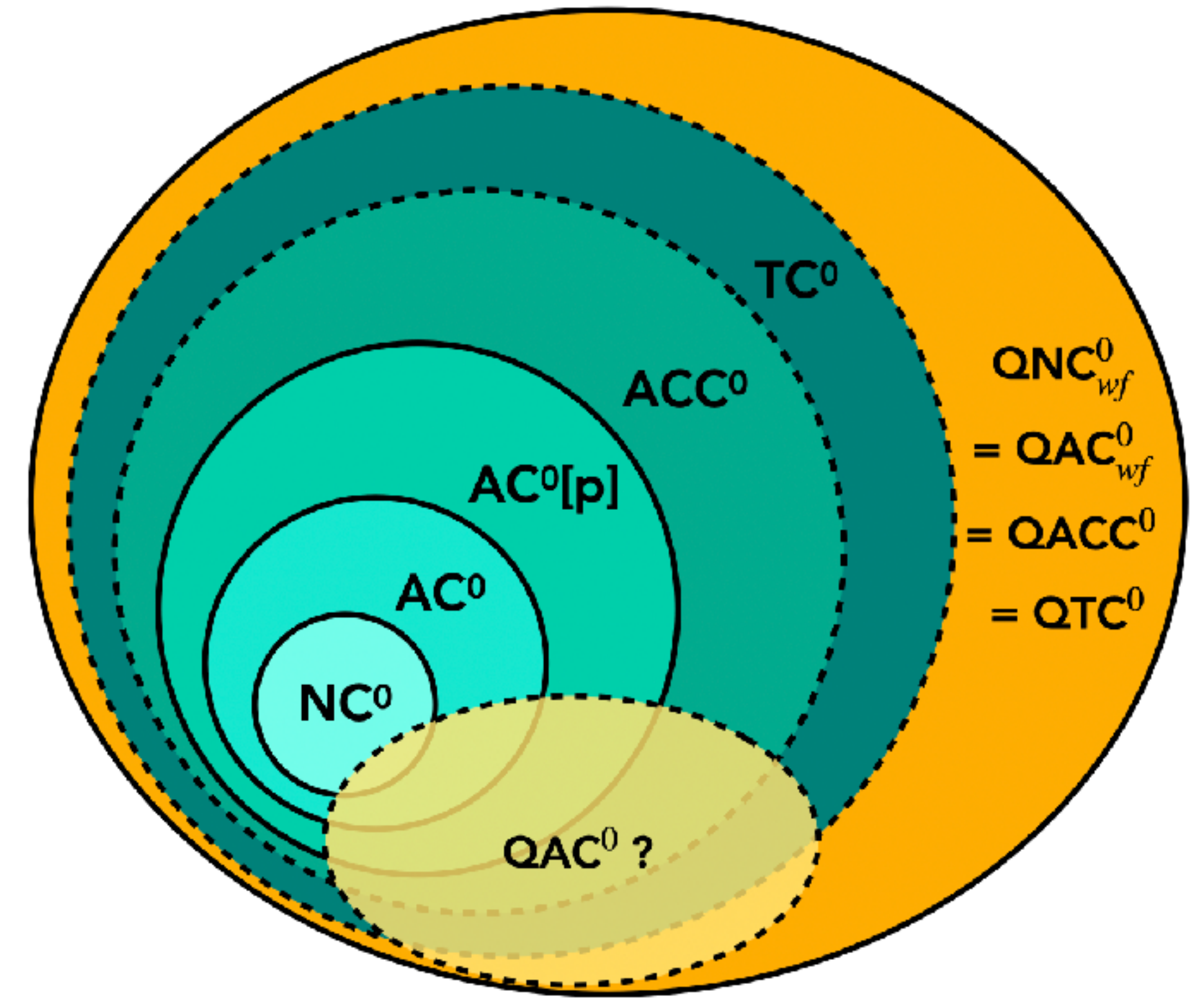
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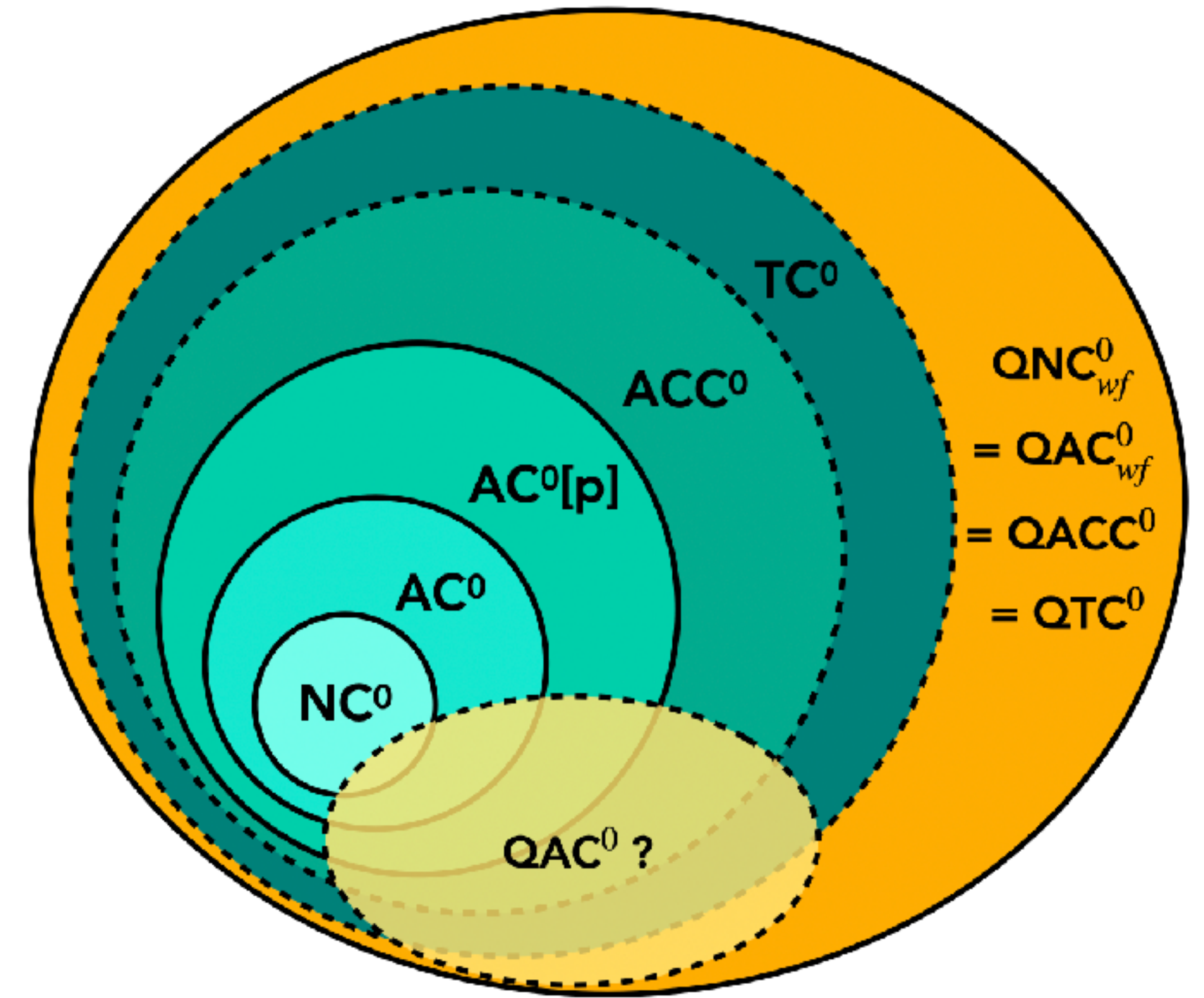
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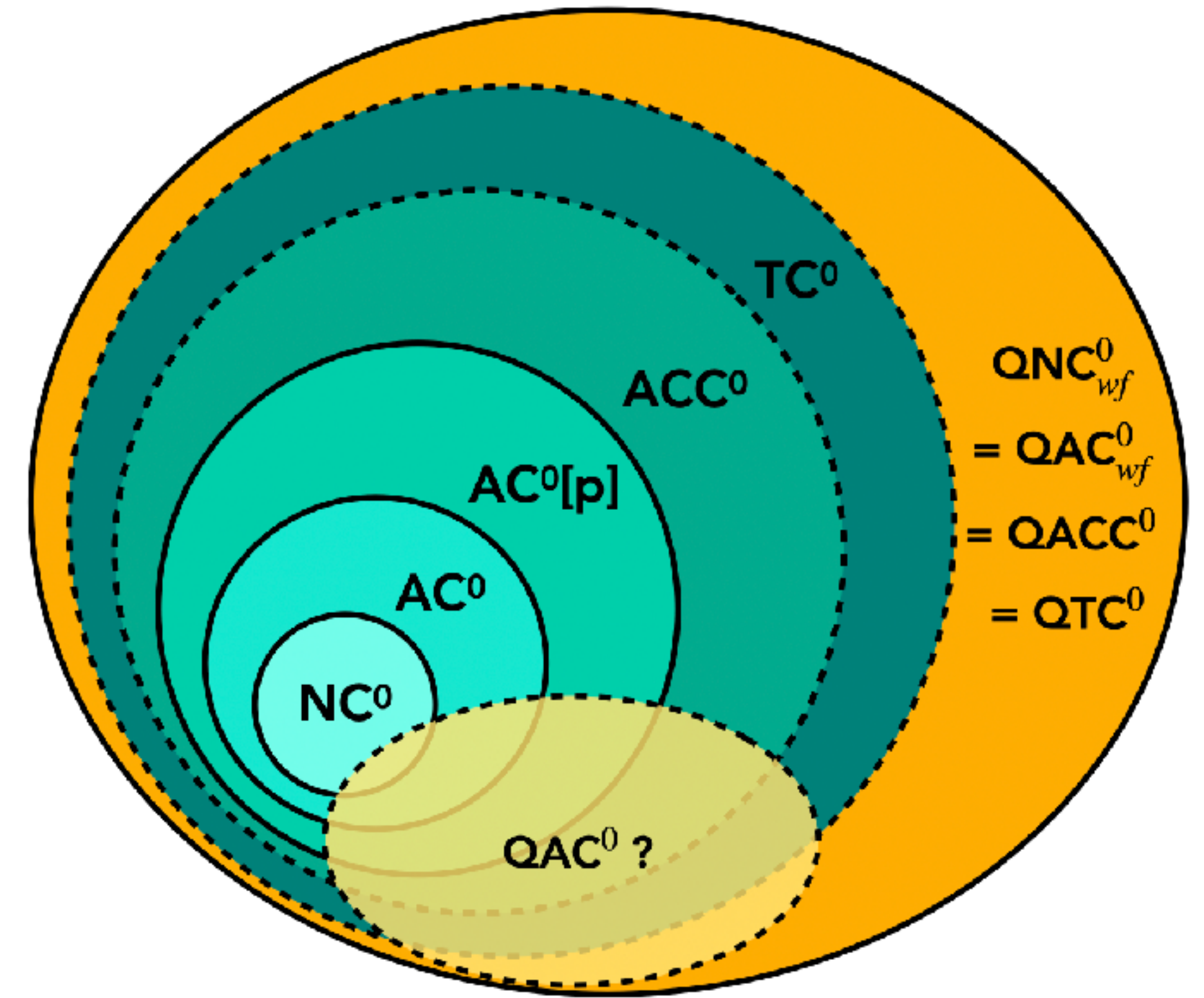
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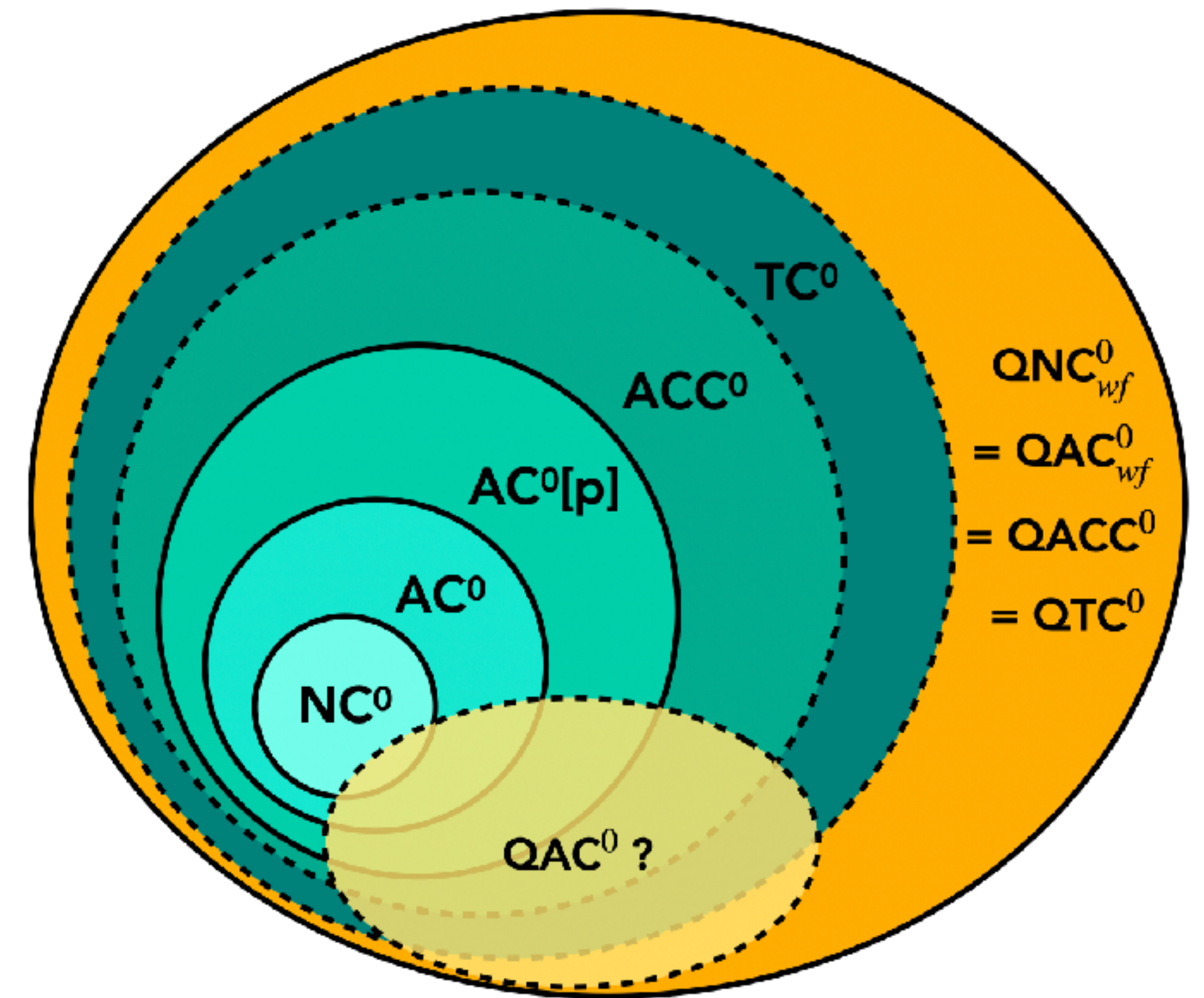
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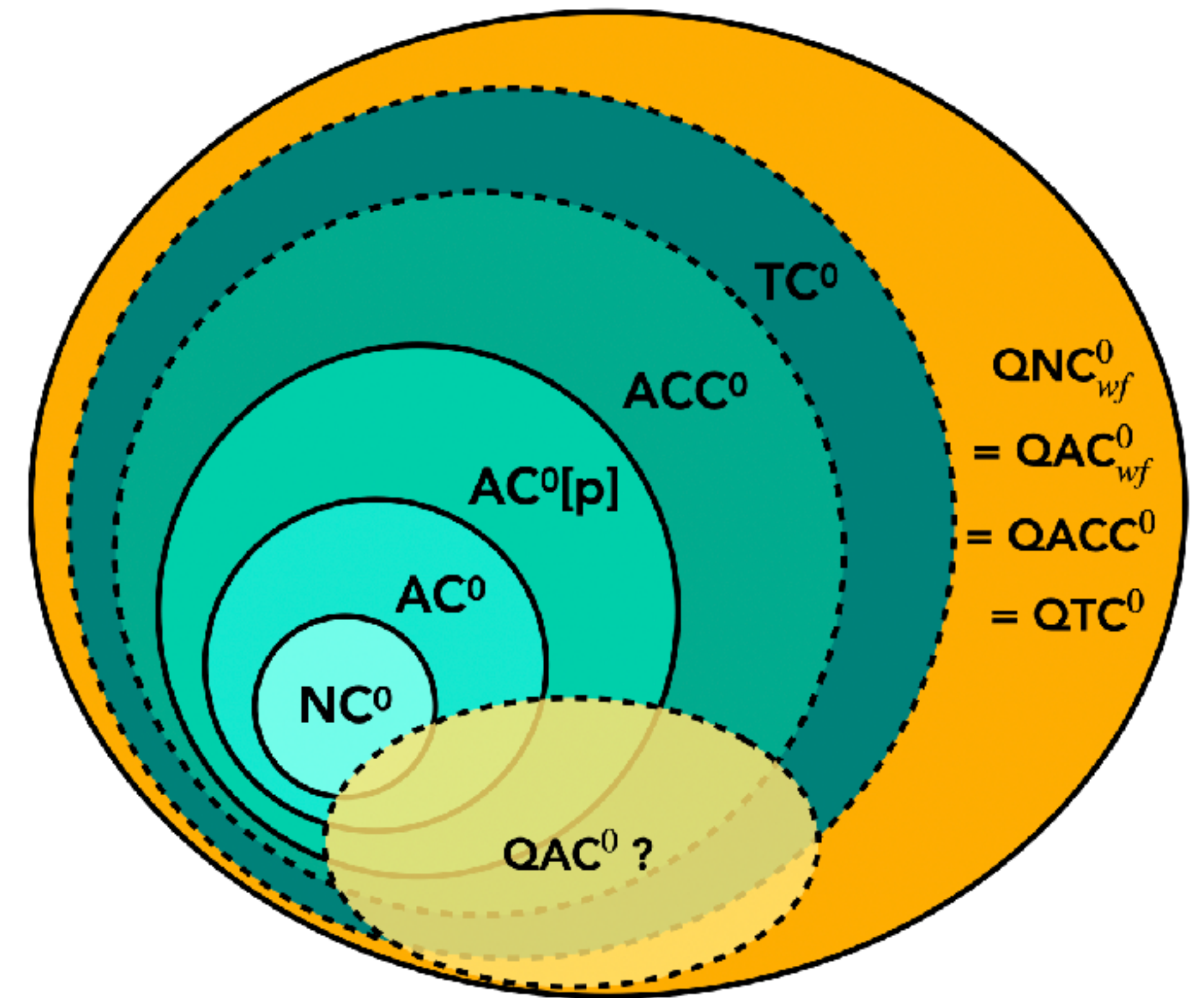
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If you like **real life**:

- **Noisy Intermediate Scale Quantum Era:** practically limited to $O(1)$ -depth.
- Are Quantum supremacy experiments viable?
Rely on **Quantum Fourier Transform**, highly entangled **symmetric states**.



QAC⁰ : How Powerful is the Parity Gate?

Parity := Output qubit of $C|x_1x_2\dots x_n\rangle|0^m\rangle$ is $|x_1 \oplus x_2 \dots \oplus x_n\rangle$

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- Quantum Fourier Transform
- Arbitrary symmetric states,
arbitrary $O(\log n)$ qubit states
and more...

Quantum circuit complexity of Parity

Prior work

Upper bounds

Lower bounds

- $O(\log n)$ -depth, local gates

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This work: Stronger QAC⁰ lower bounds

Depth 3 results

- Majority $\notin \text{QAC}^0(\exp o(\sqrt{n}), 3)$ (needs **exponential size**)
- Parity $\notin \text{QAC}^0(\infty, 3)$

Depth 2 results

- Any output qubit has $O(\log n)$ **total influence**
 $\implies$ no approx. Parity. (Inf[Parity] = n)
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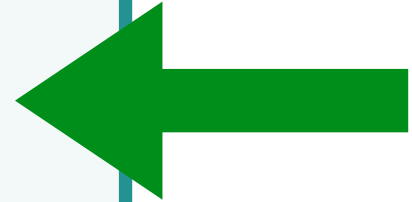
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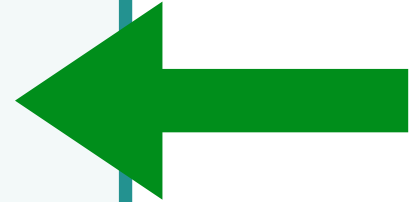
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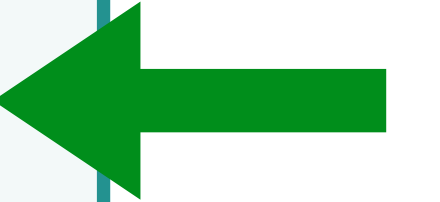
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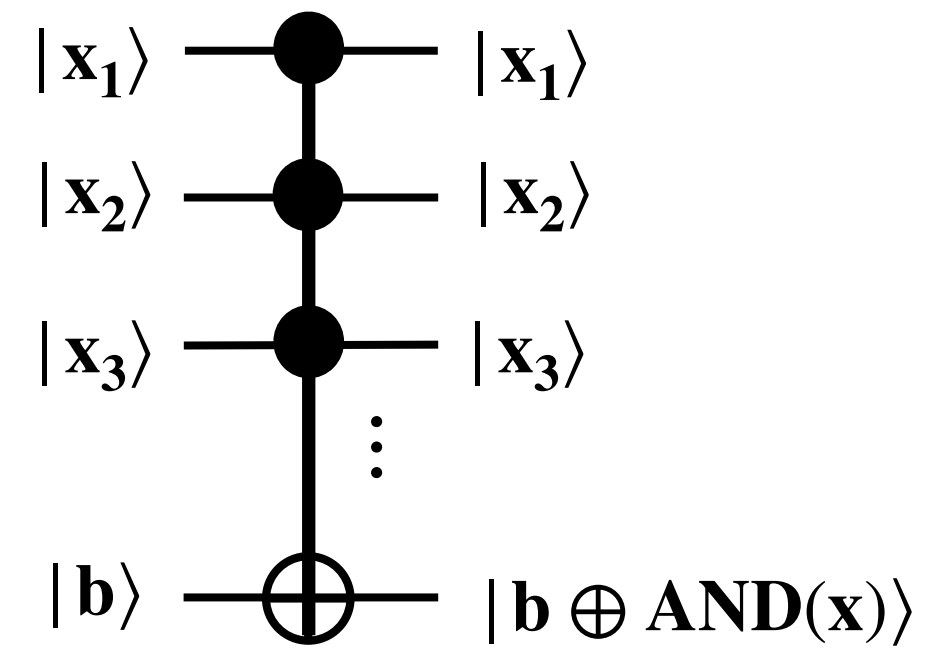
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Exact lower bounds suffice!

Background: QAC⁰ canonical form [Ros'21]

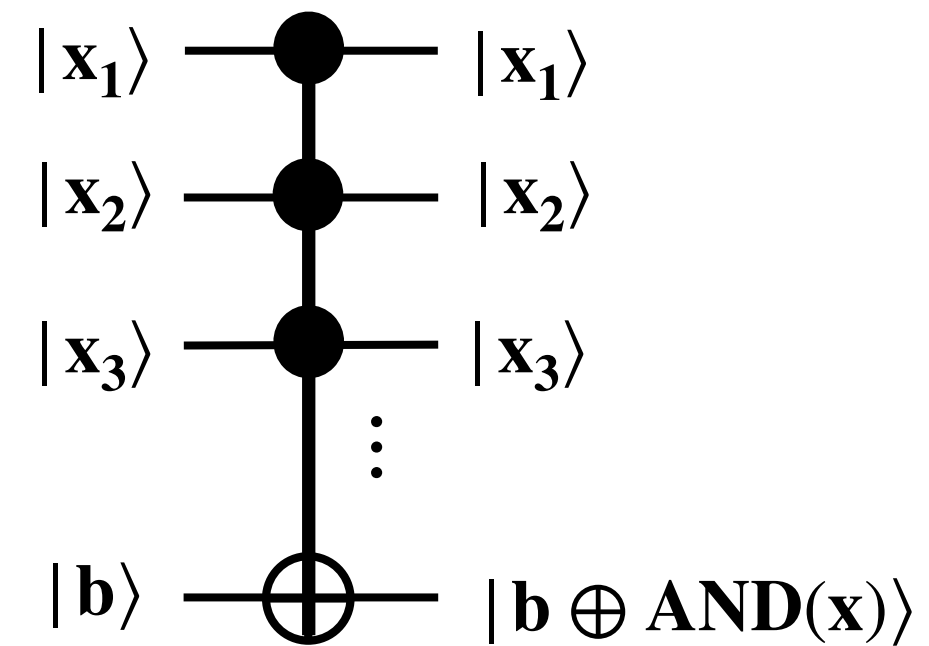
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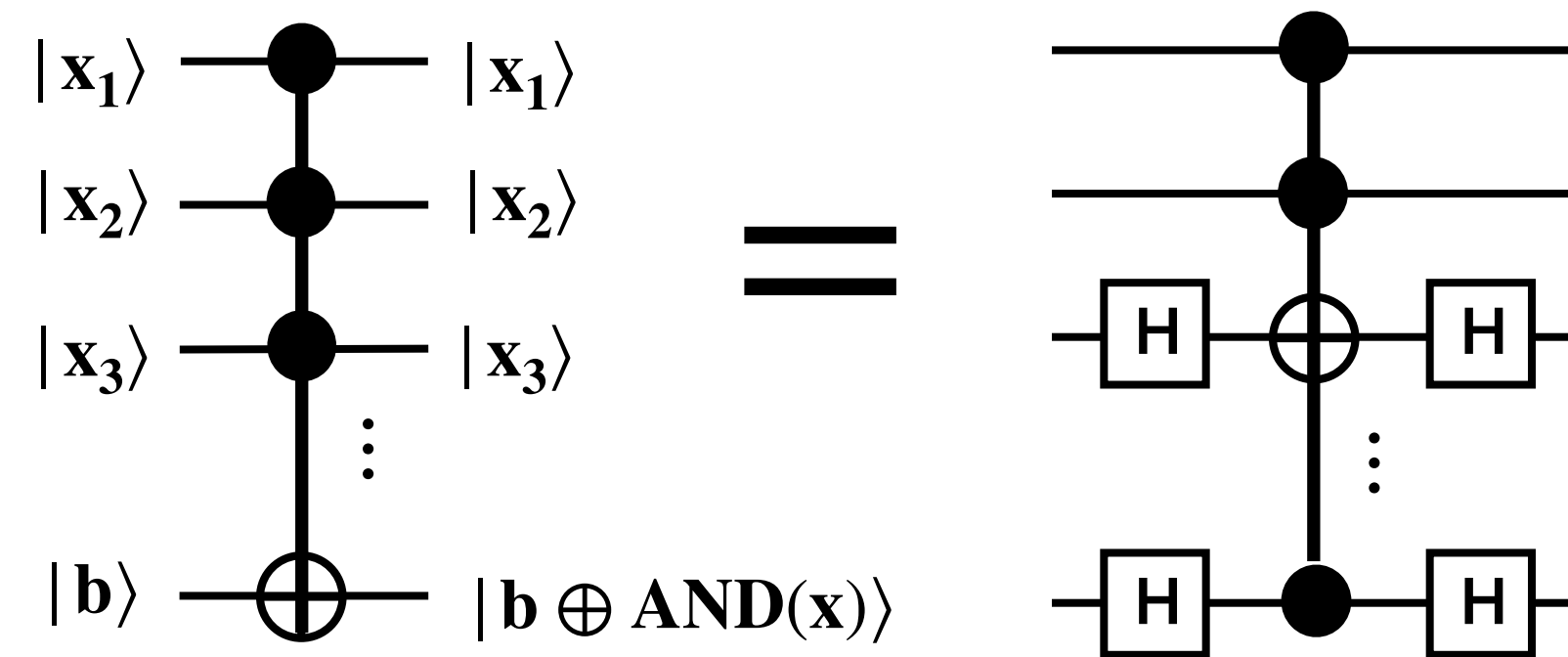
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Single qubit unitaries $\longrightarrow$ no special output

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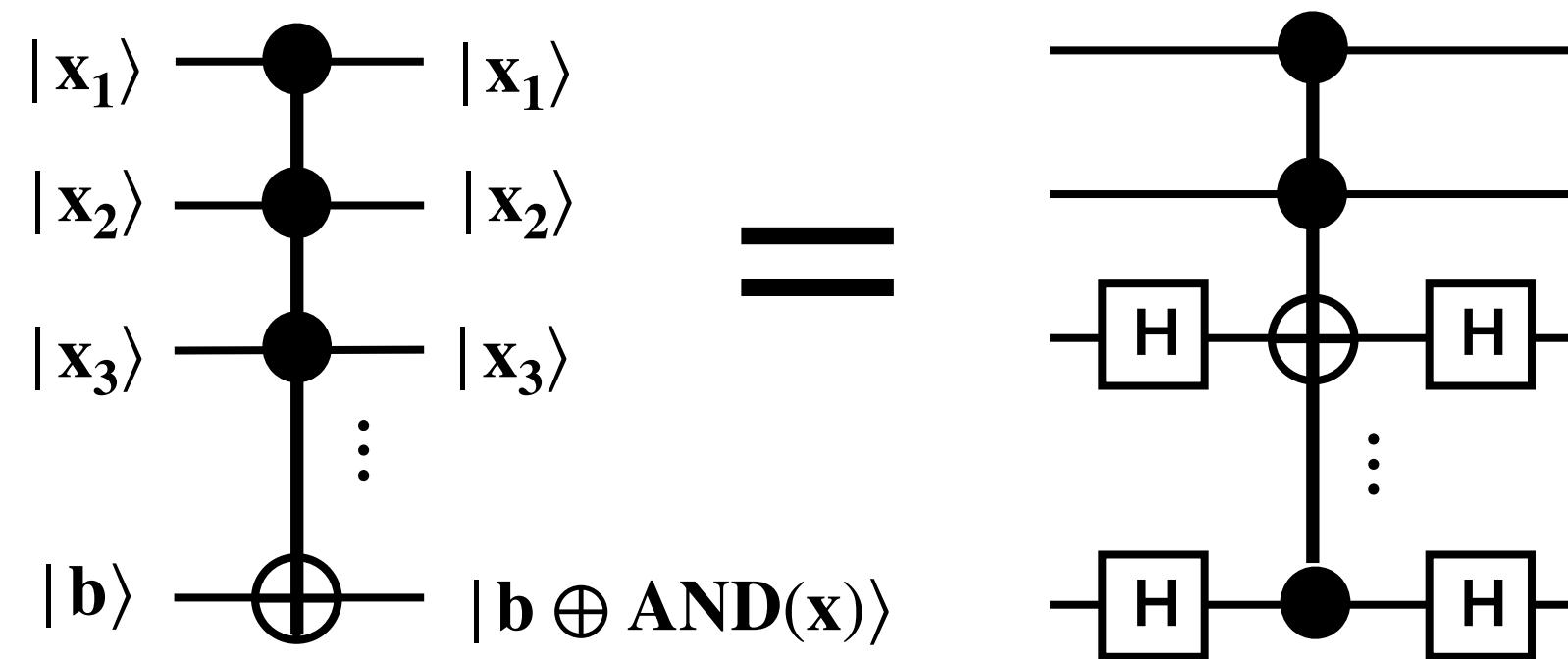
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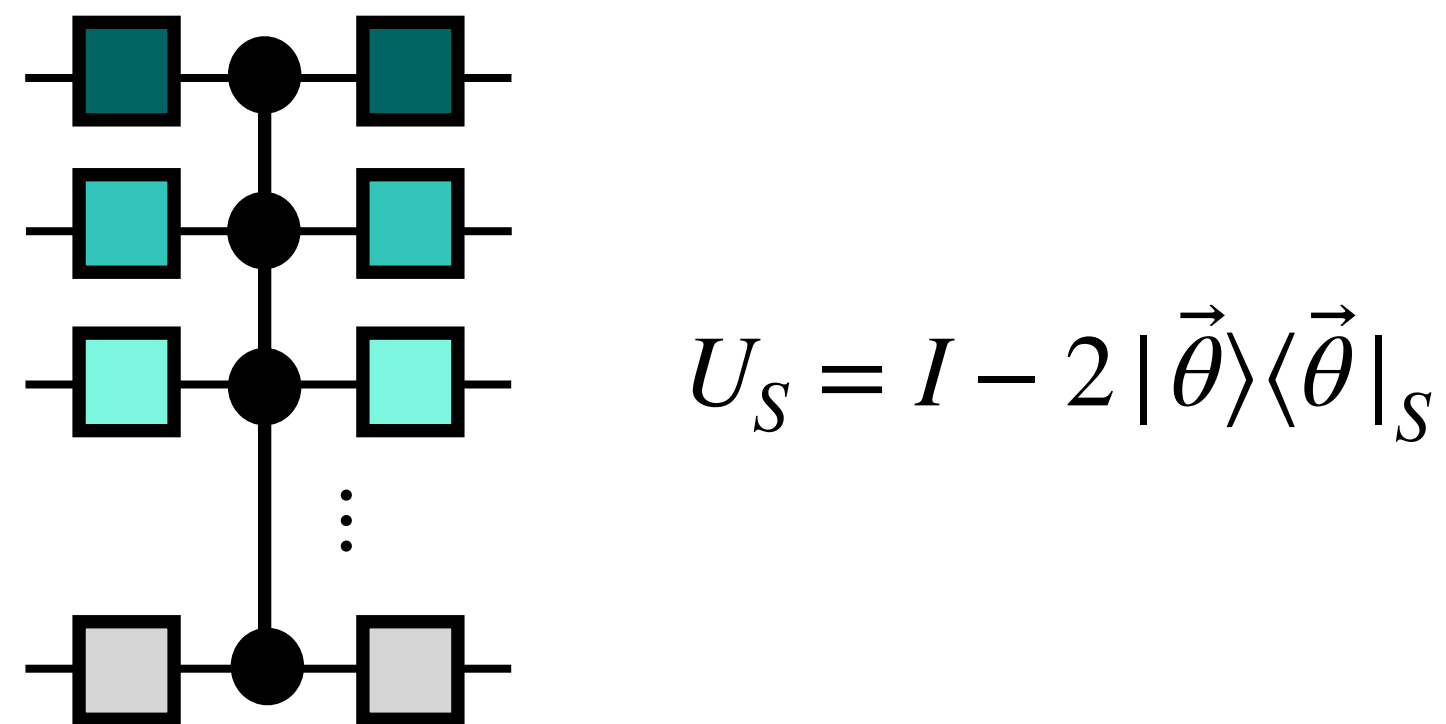
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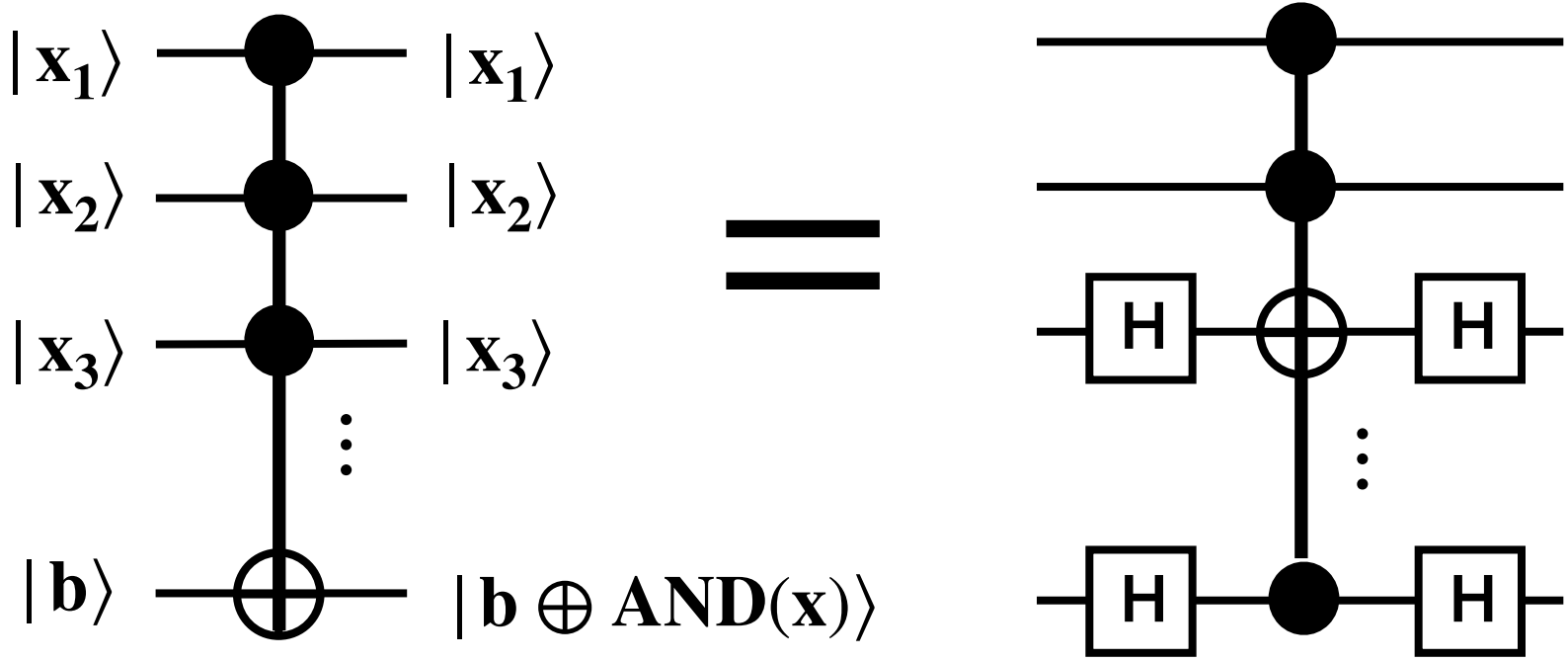
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Reflections about product state



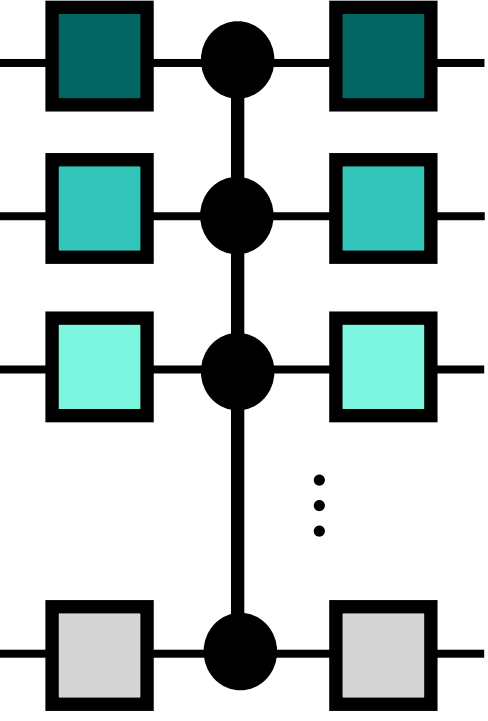
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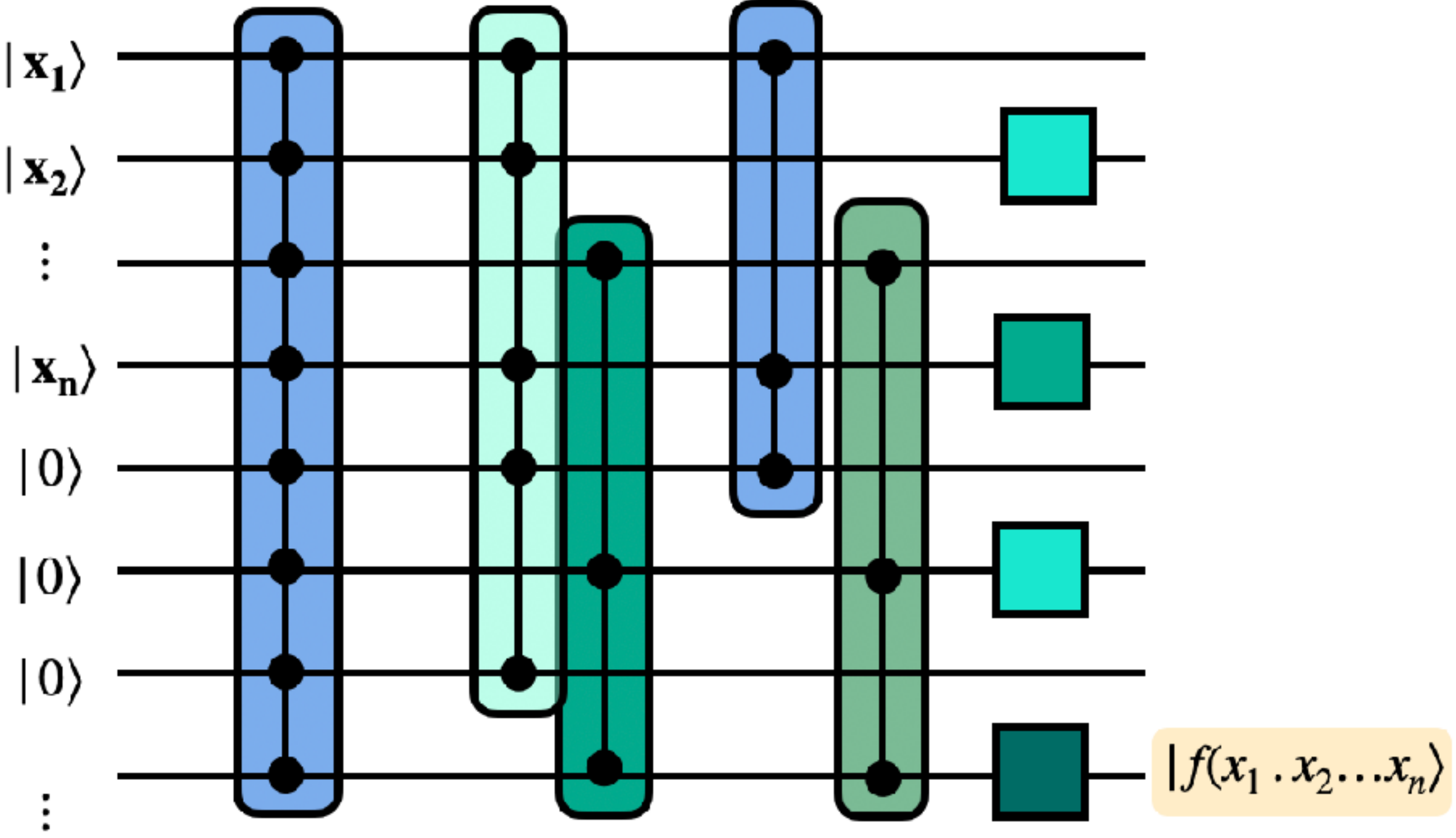
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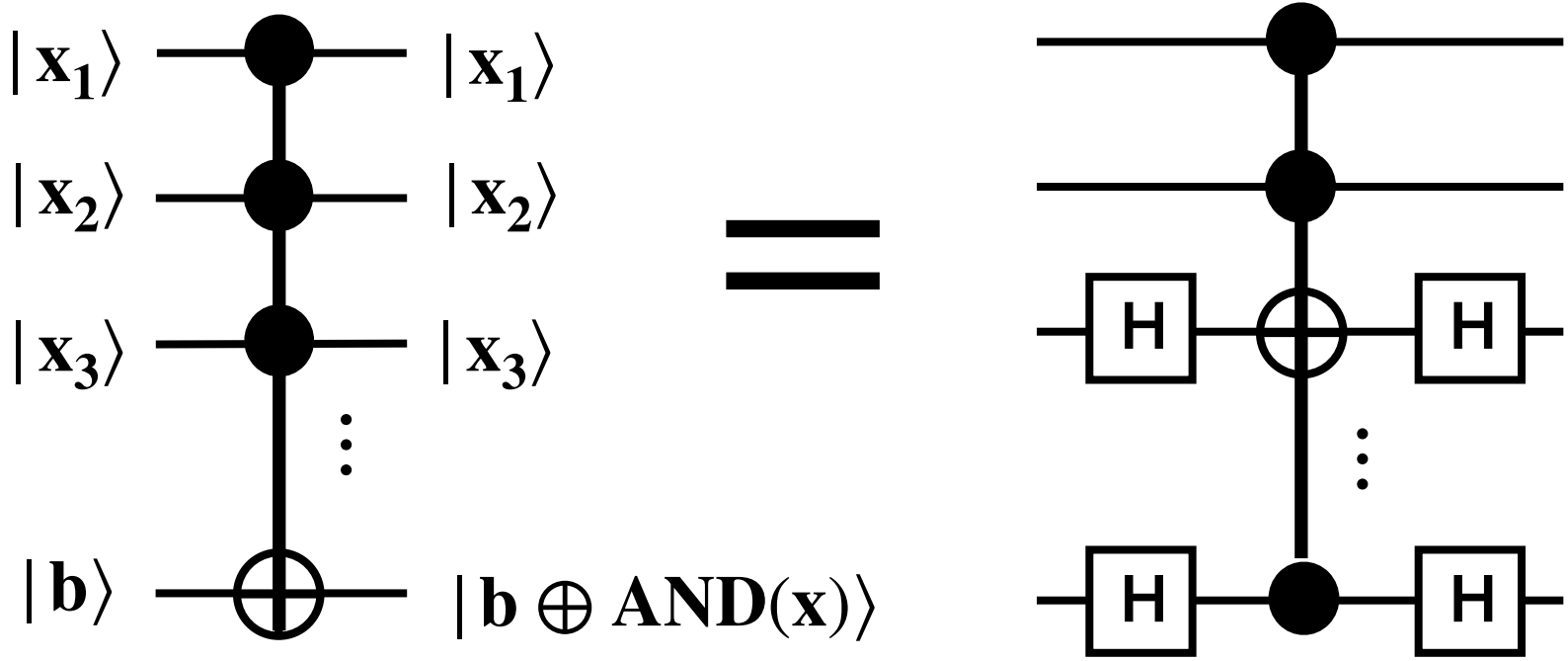
$$U_S = I - 2|\vec{\theta}\rangle\langle\vec{\theta}|_S$$

$\text{QAC}^0(d, s) := d$ layers of s reflection gates
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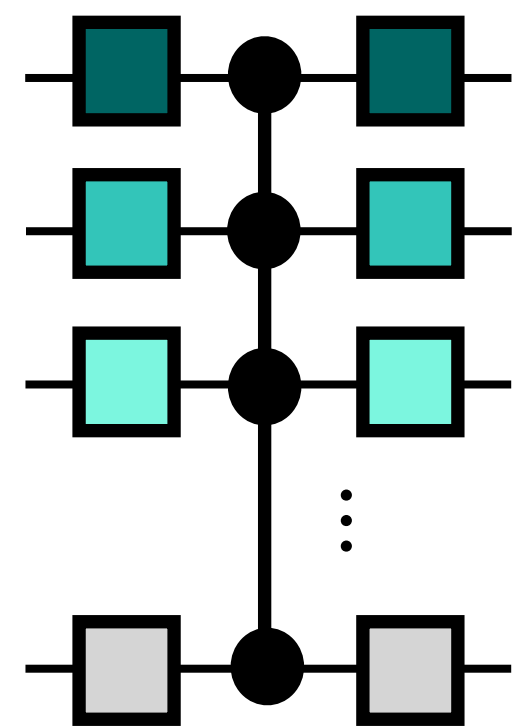
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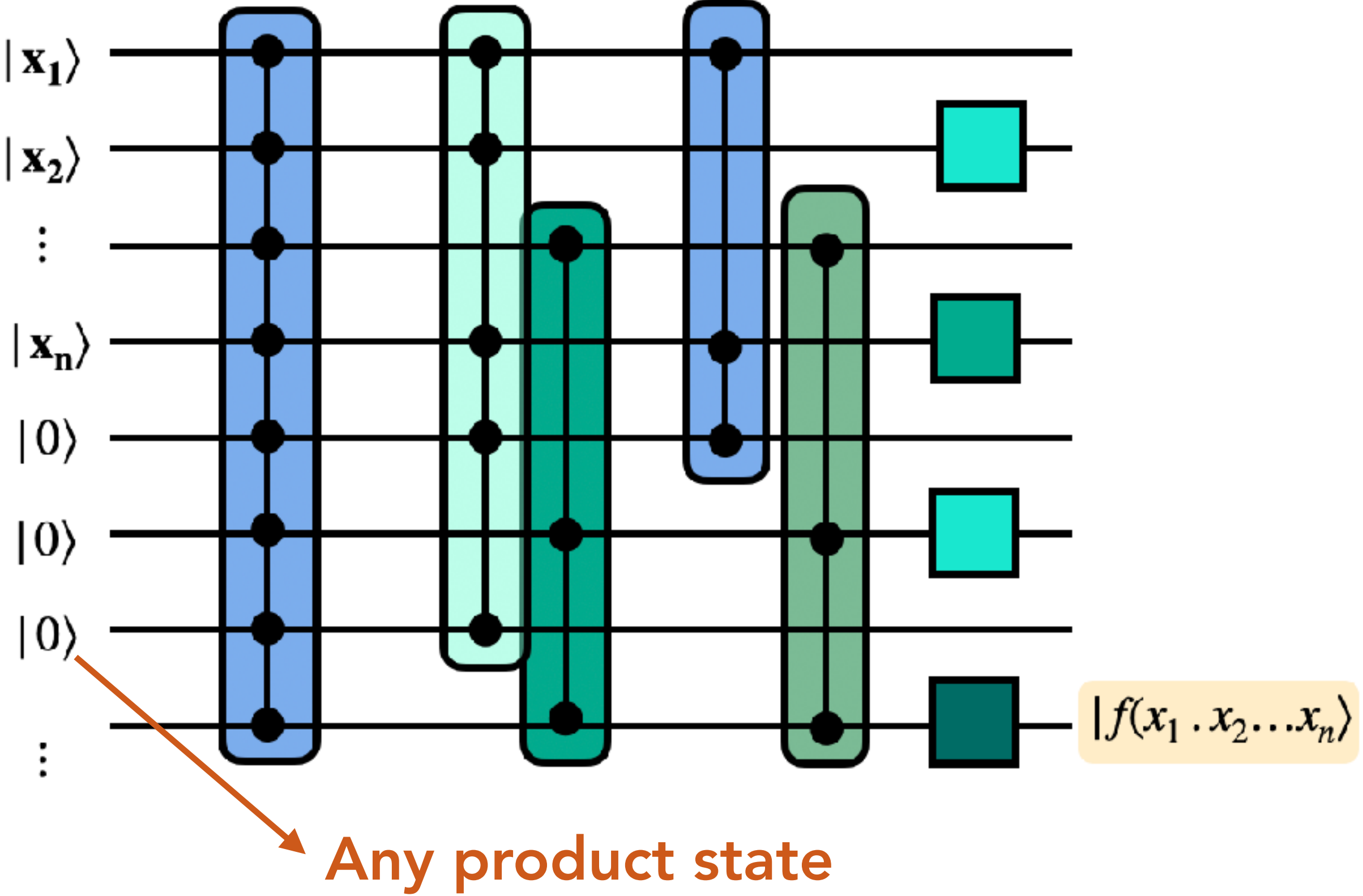
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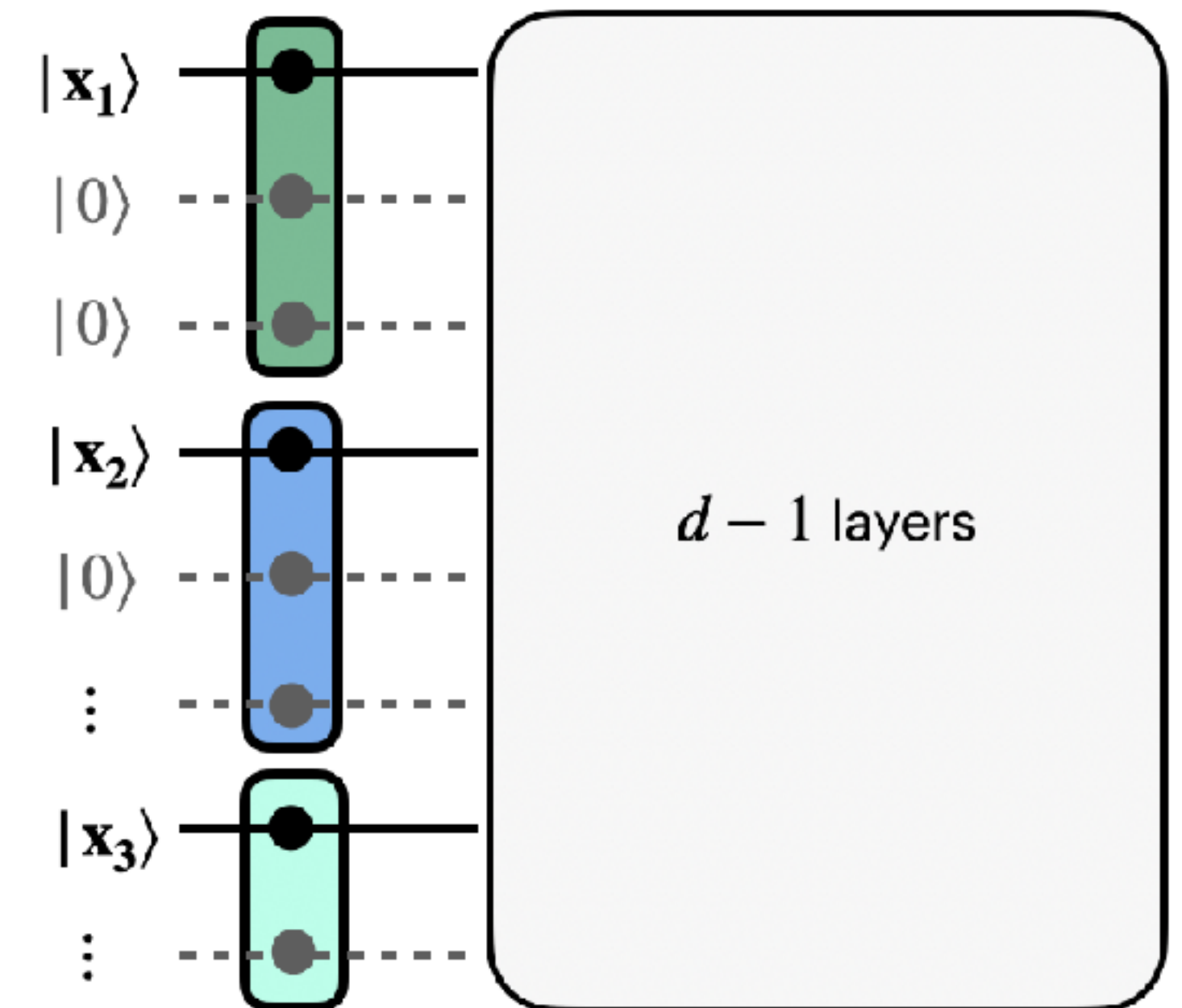


Parity, Majority $\notin \text{QAC}^0(\exp o(\sqrt{n}), 3)$

Proof Sketch:

cleaned-up-QAC⁰

≤ 1 input wire per Layer 1 gate



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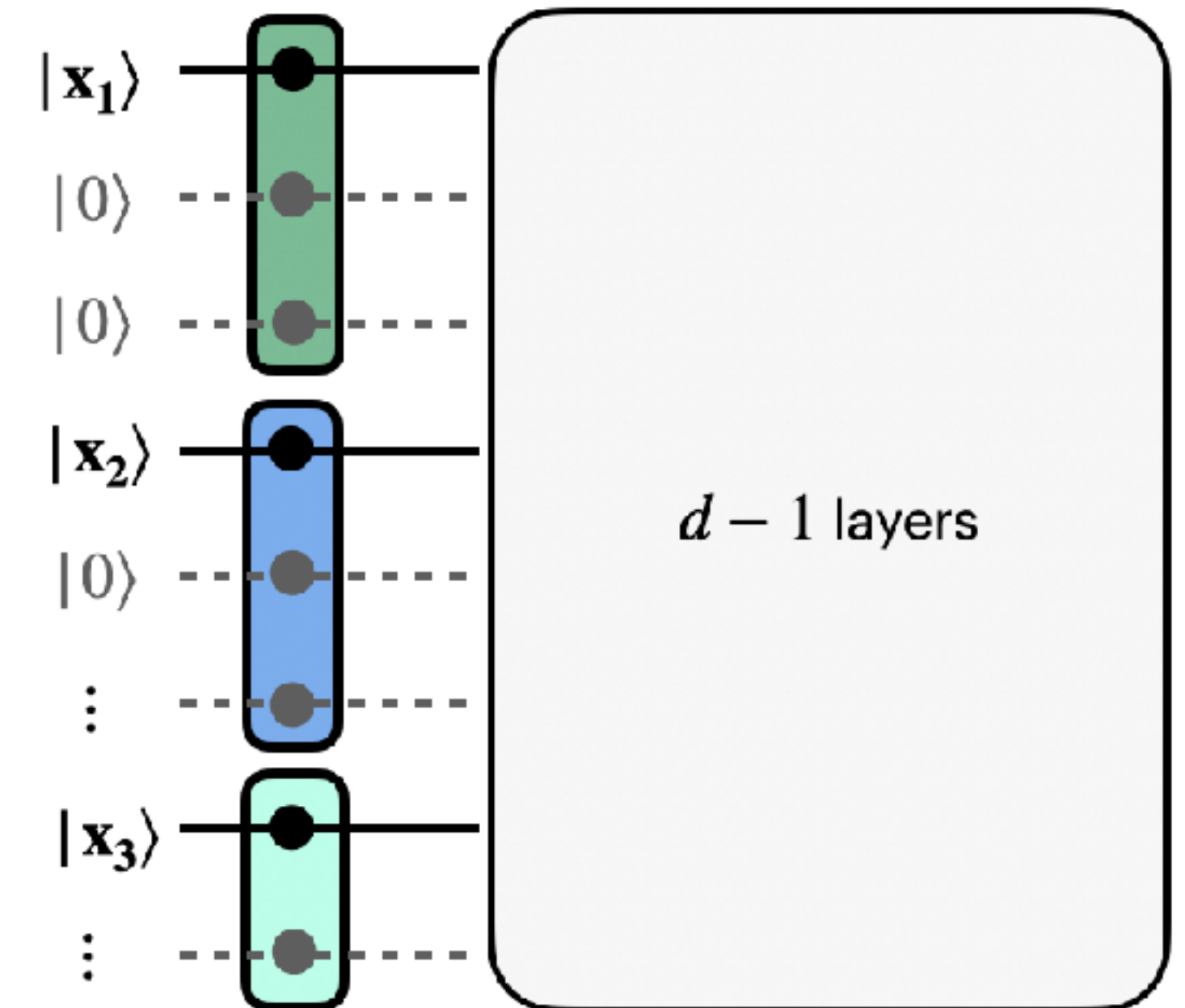
1 Clean-up lemma

$f(x) = \text{Parity/ Majority:}$

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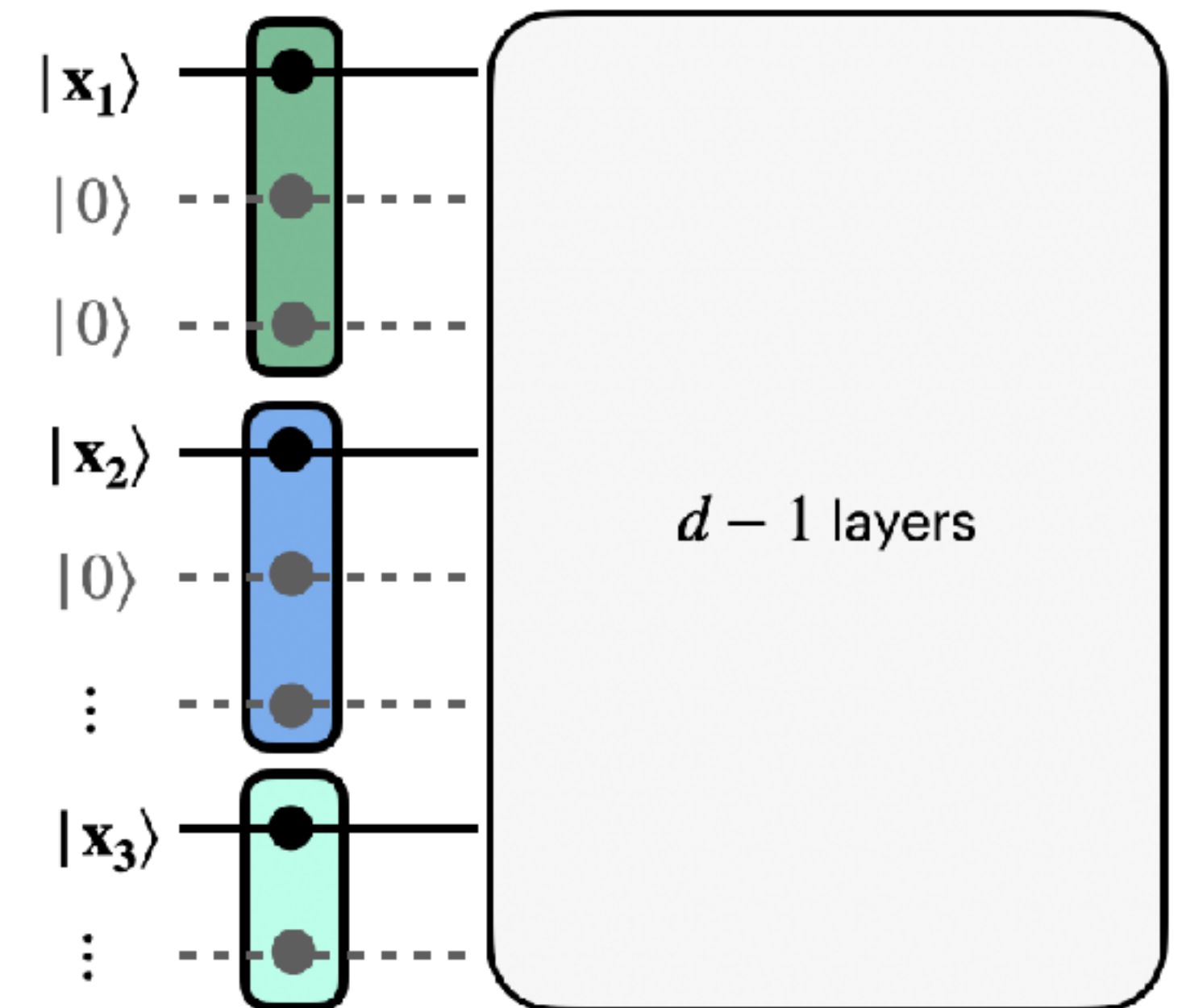
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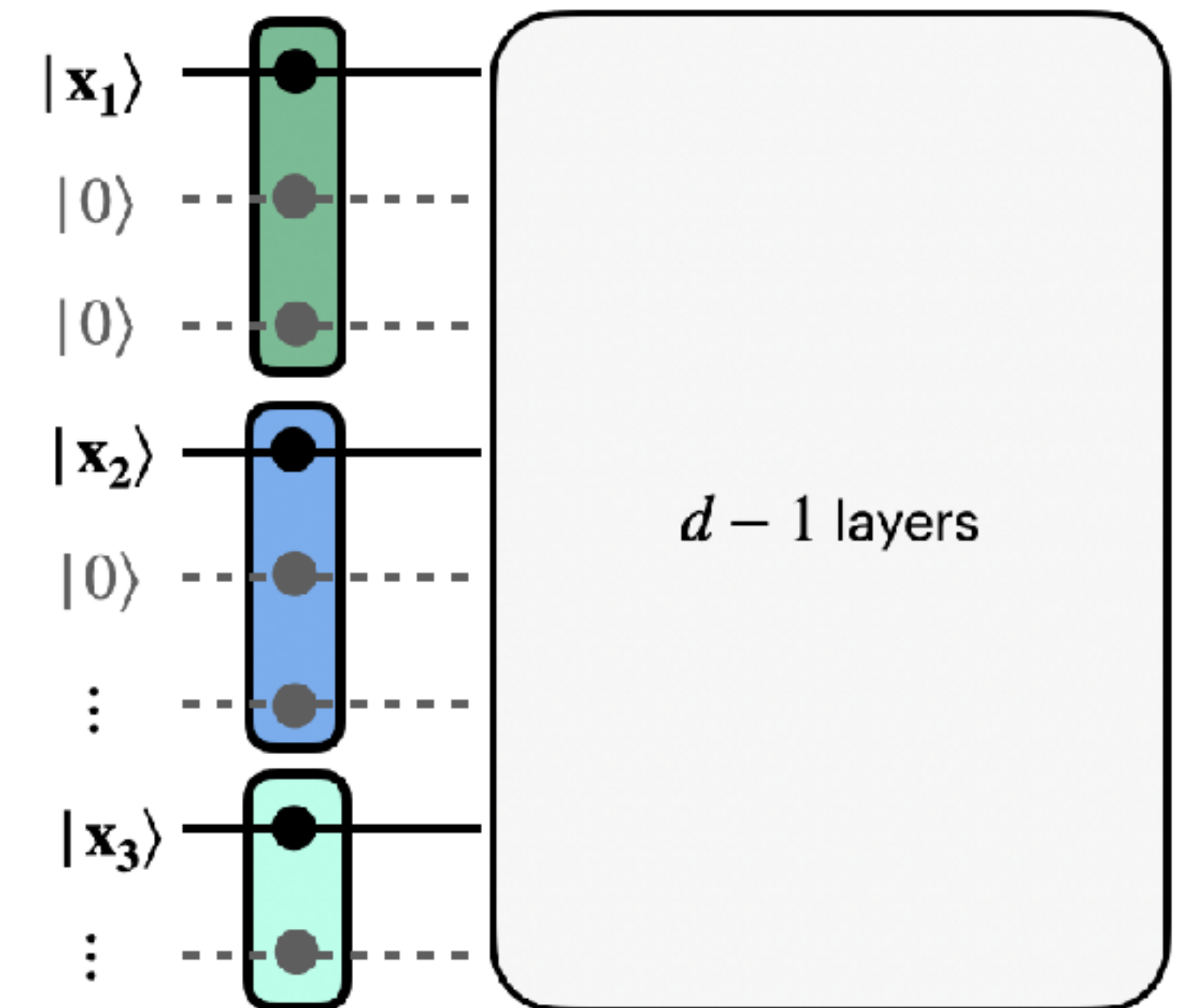
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The clean up lemma

Lemma 1: Suppose $\exists C$ a depth- d , size- s QAC^0 circuit C computing Parity of n -inputs.

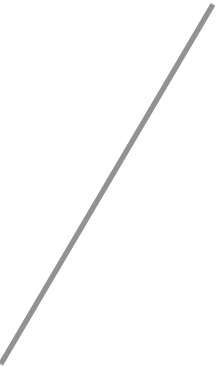
Then, $\exists$ a depth- d , size- s , **cleaned-up- QAC^0** circuit C' for computing Parity of $n' \geq n/3$ inputs.



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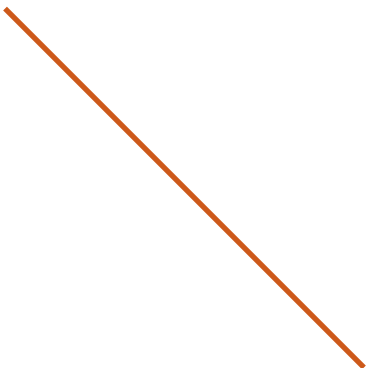
The clean up lemma

also Majority



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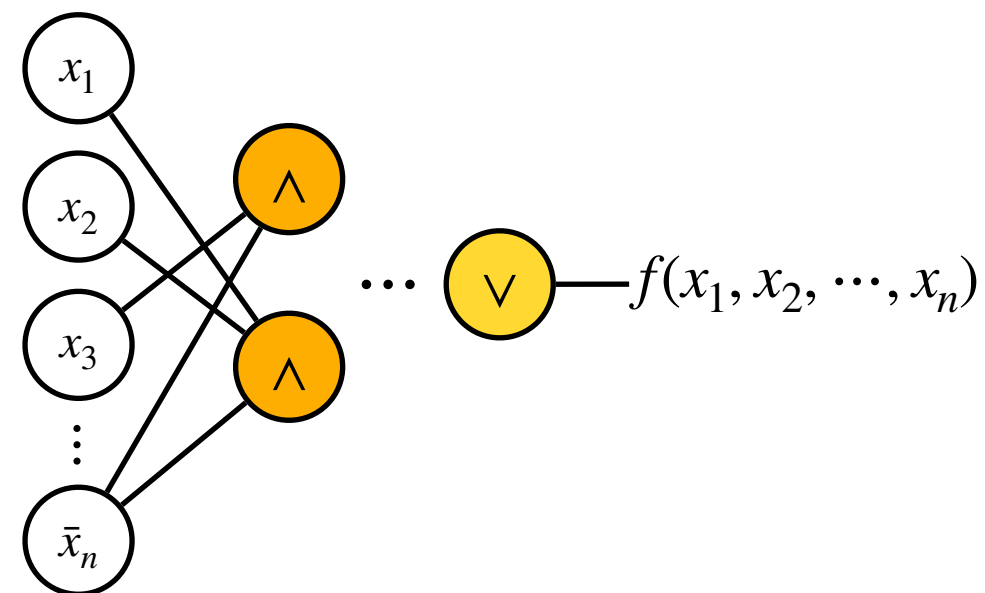
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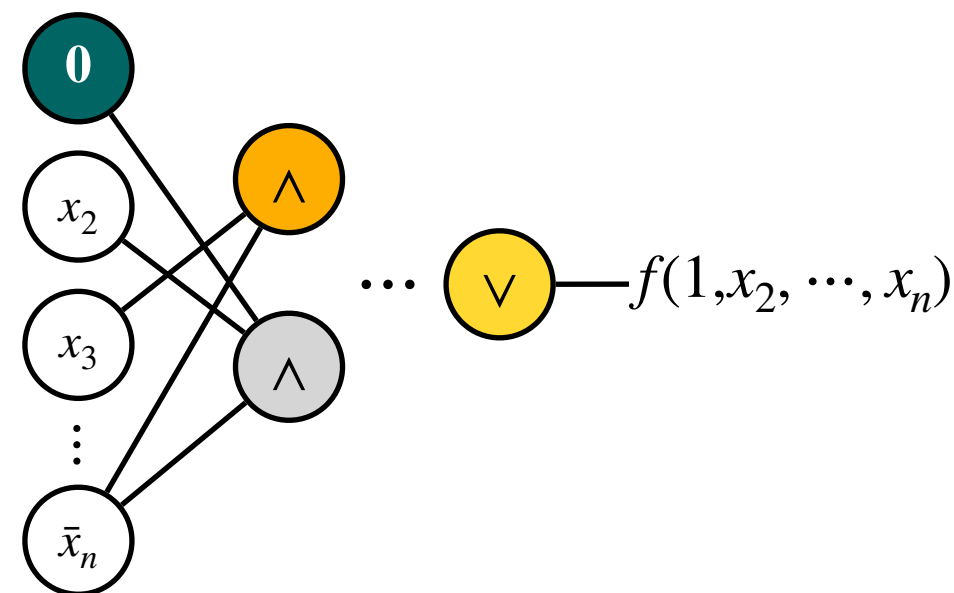


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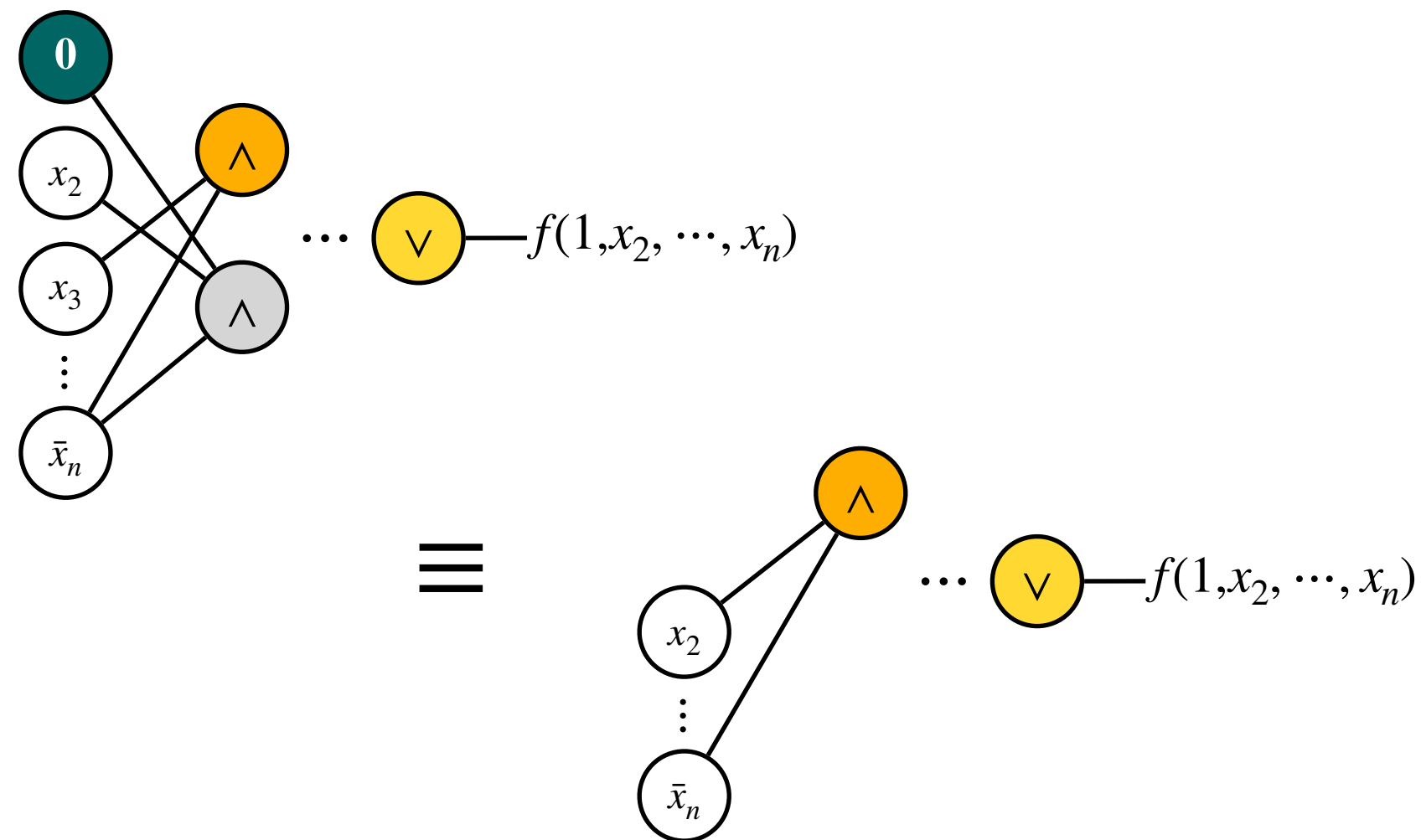


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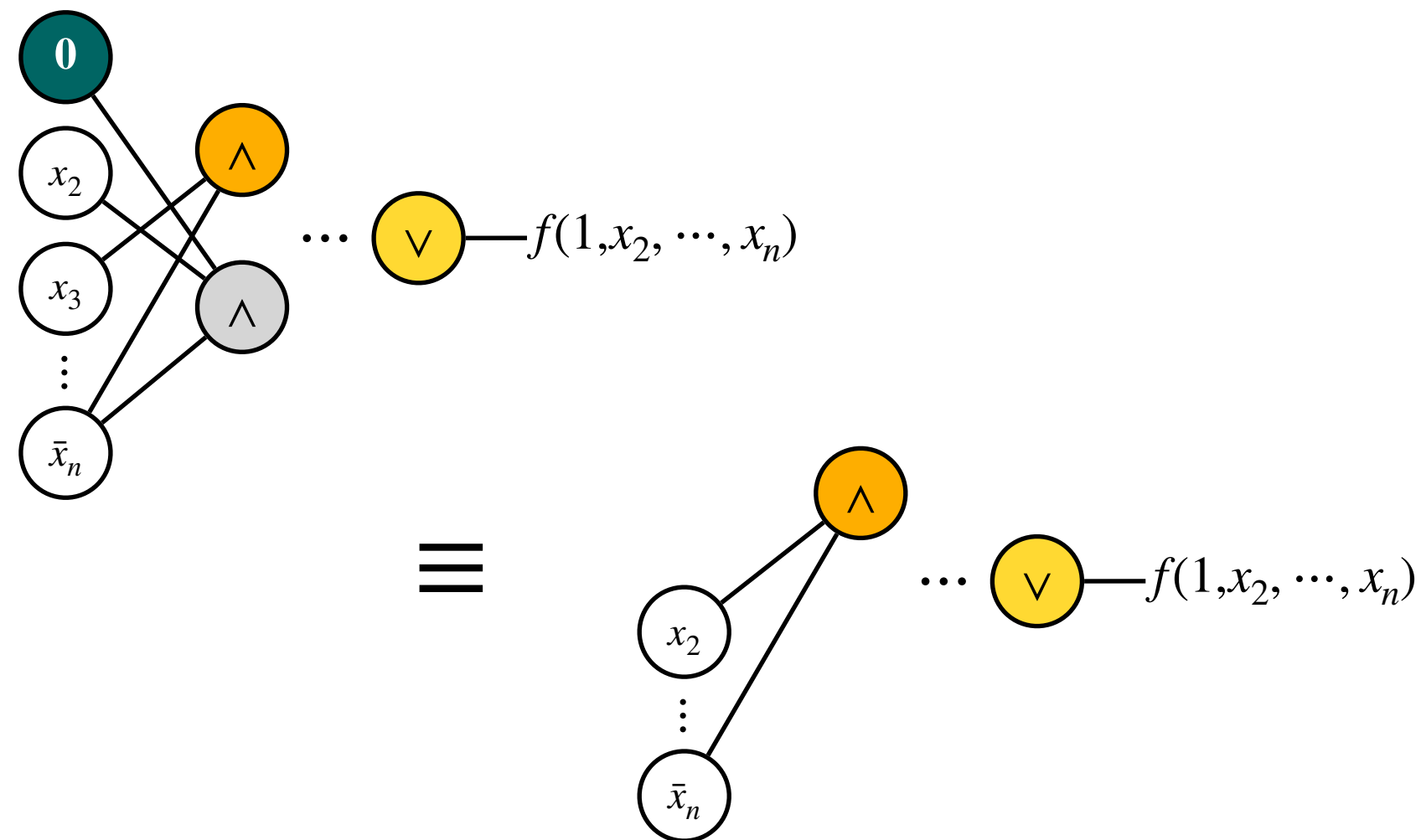


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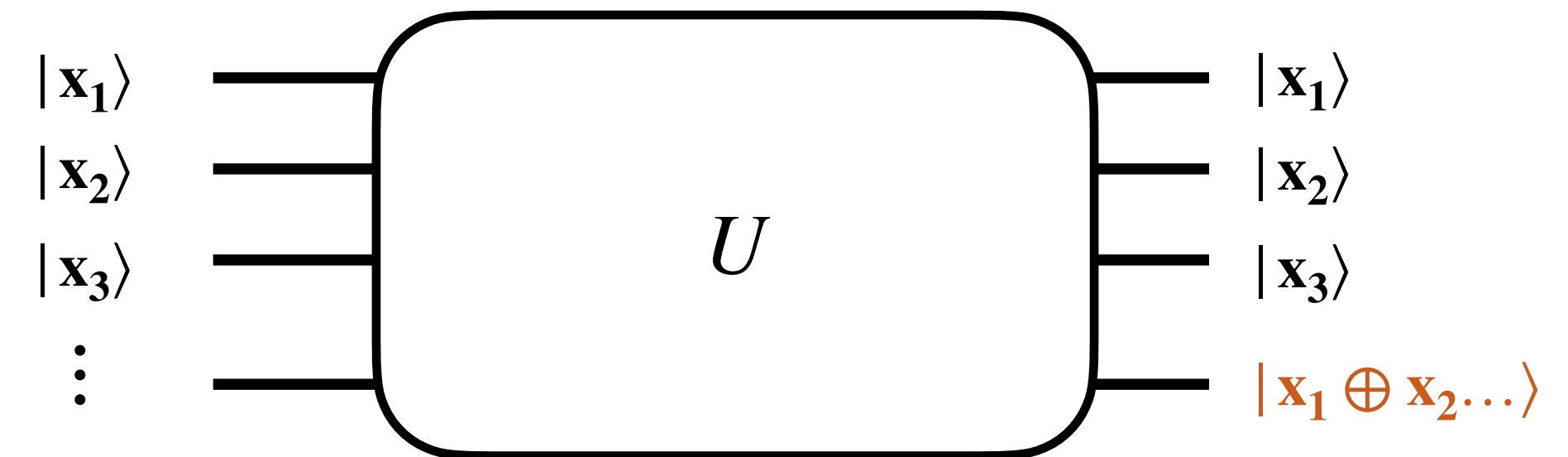
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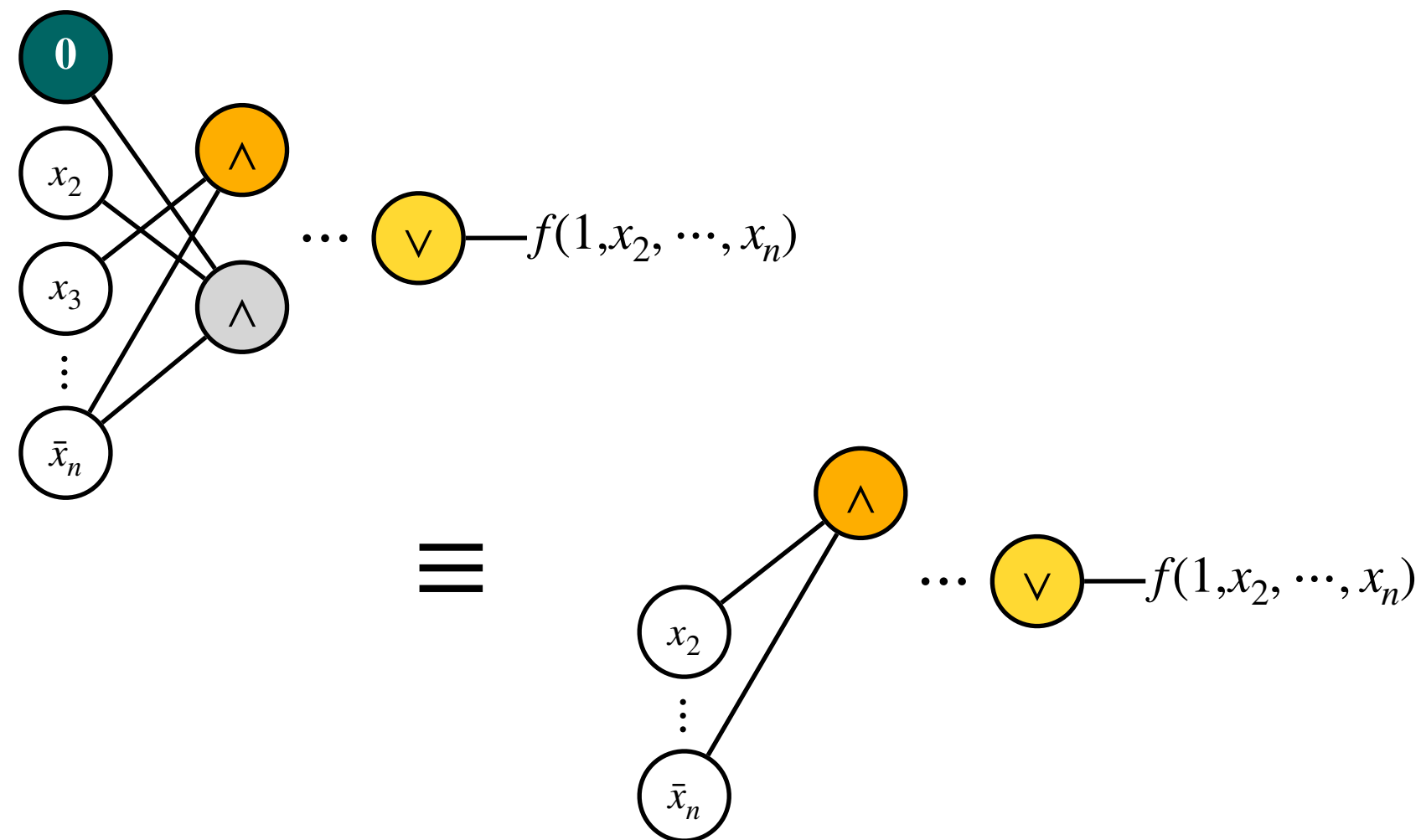


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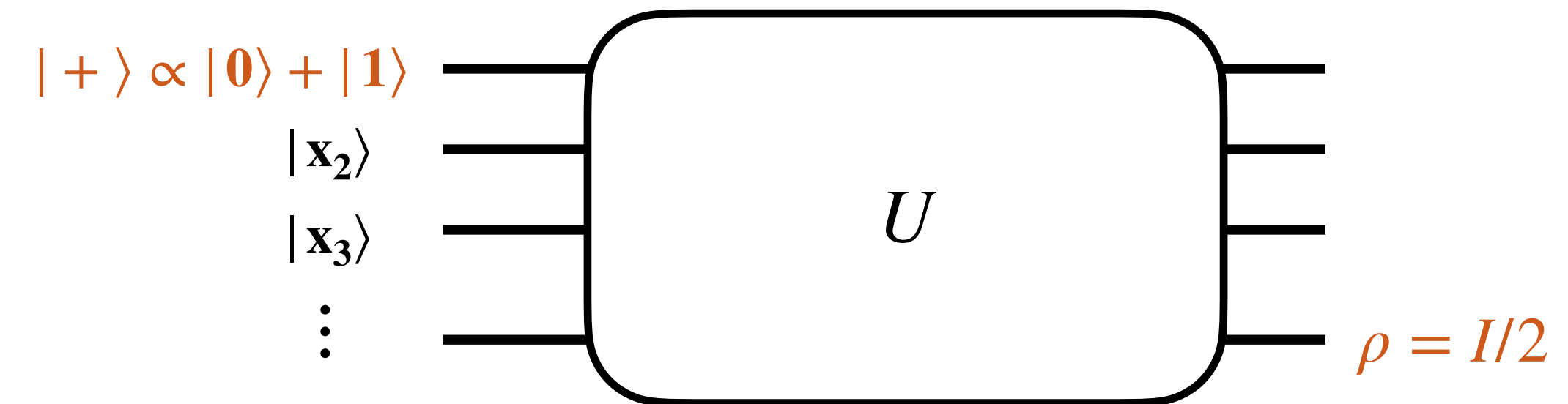
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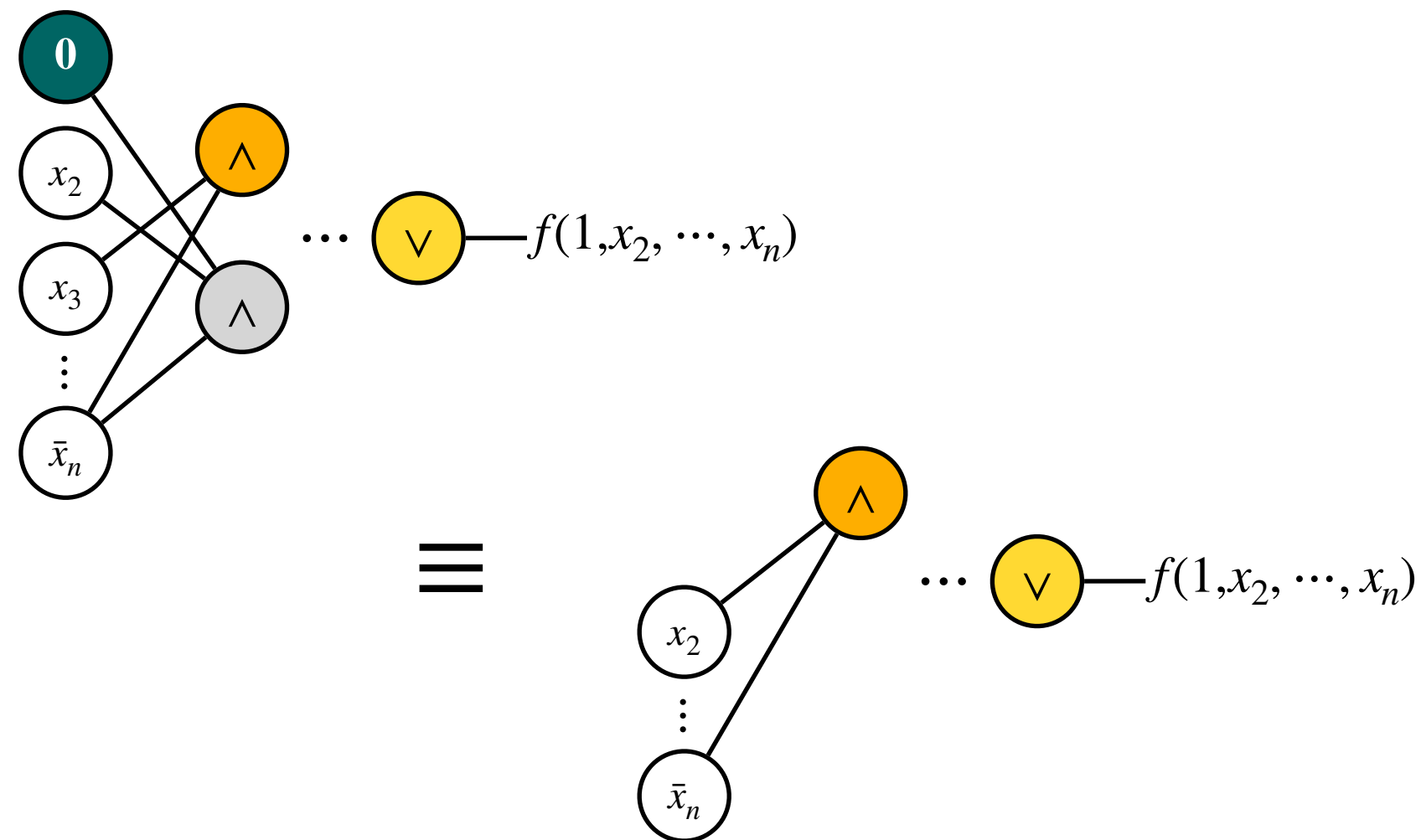


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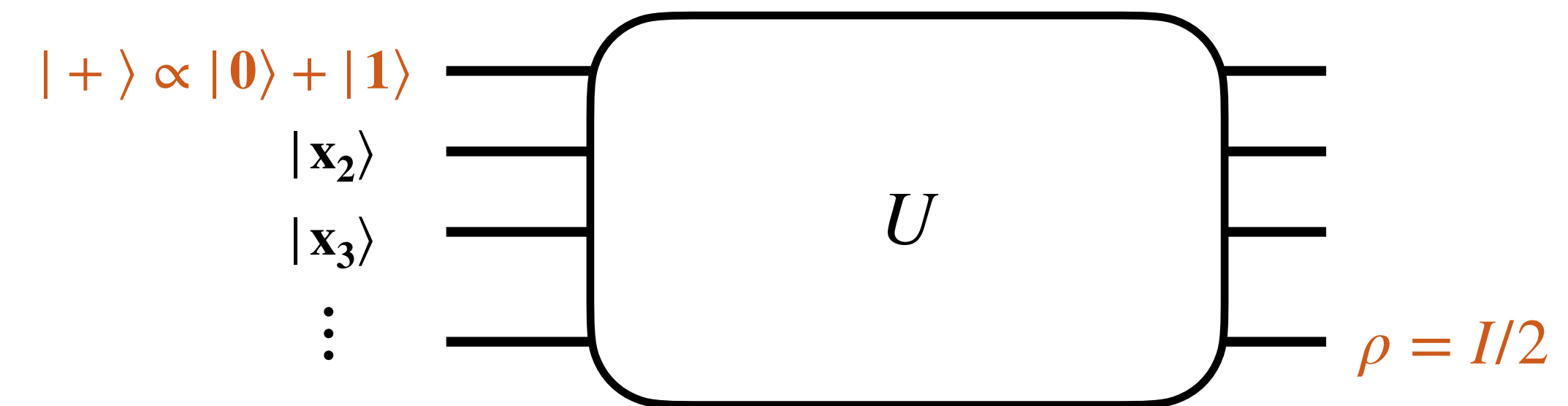
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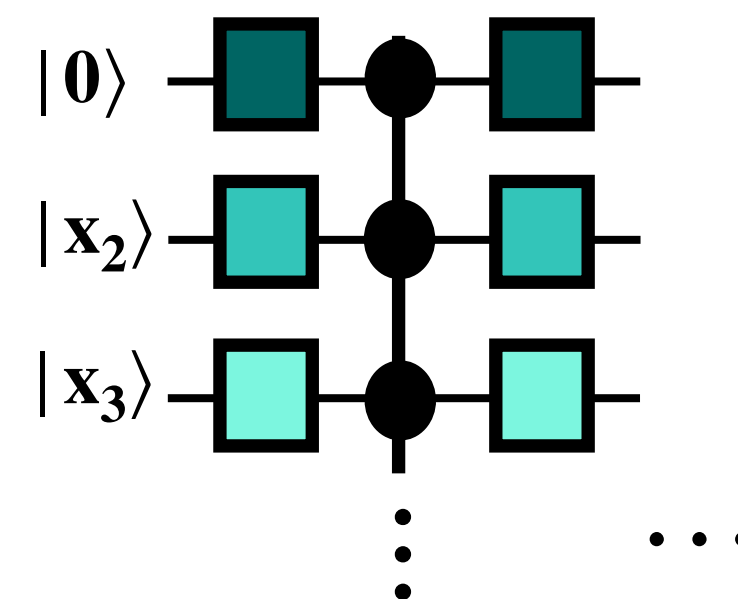
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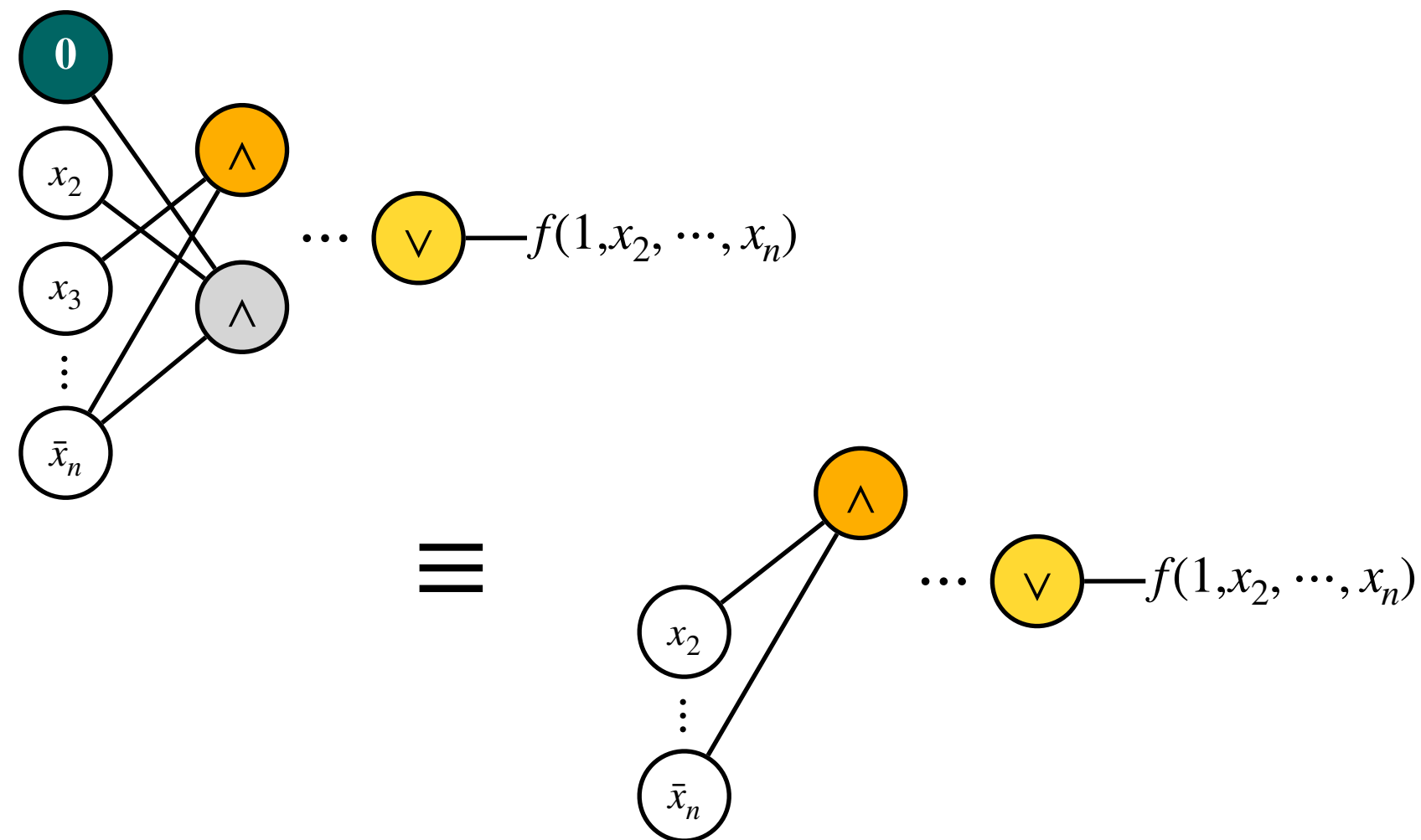


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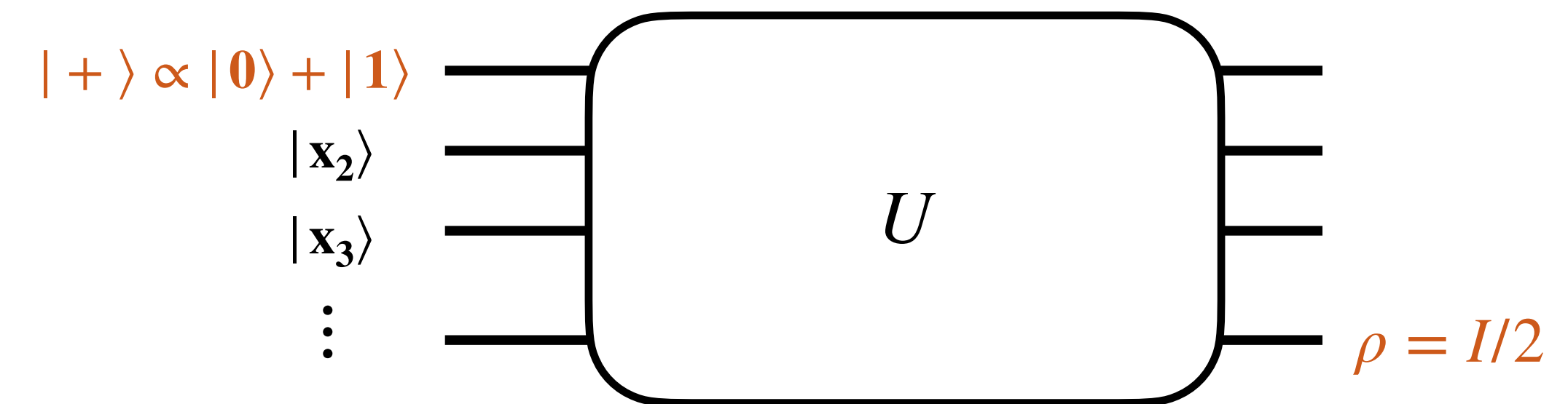
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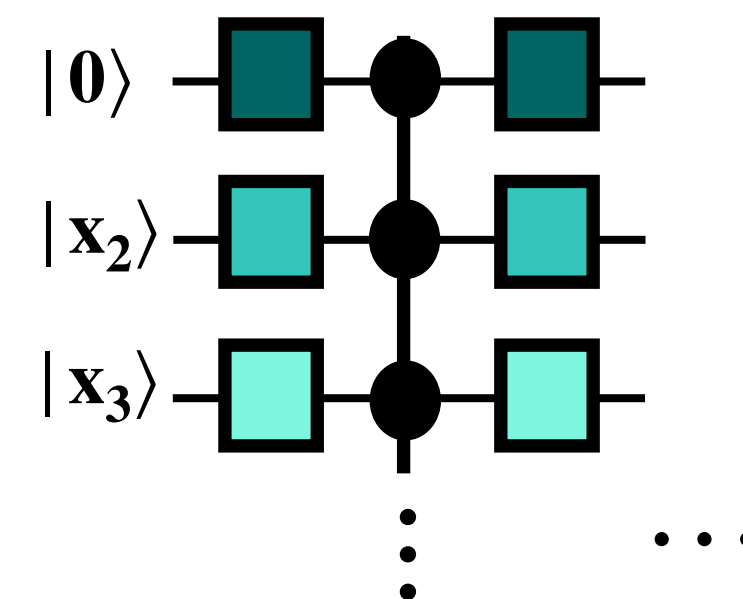
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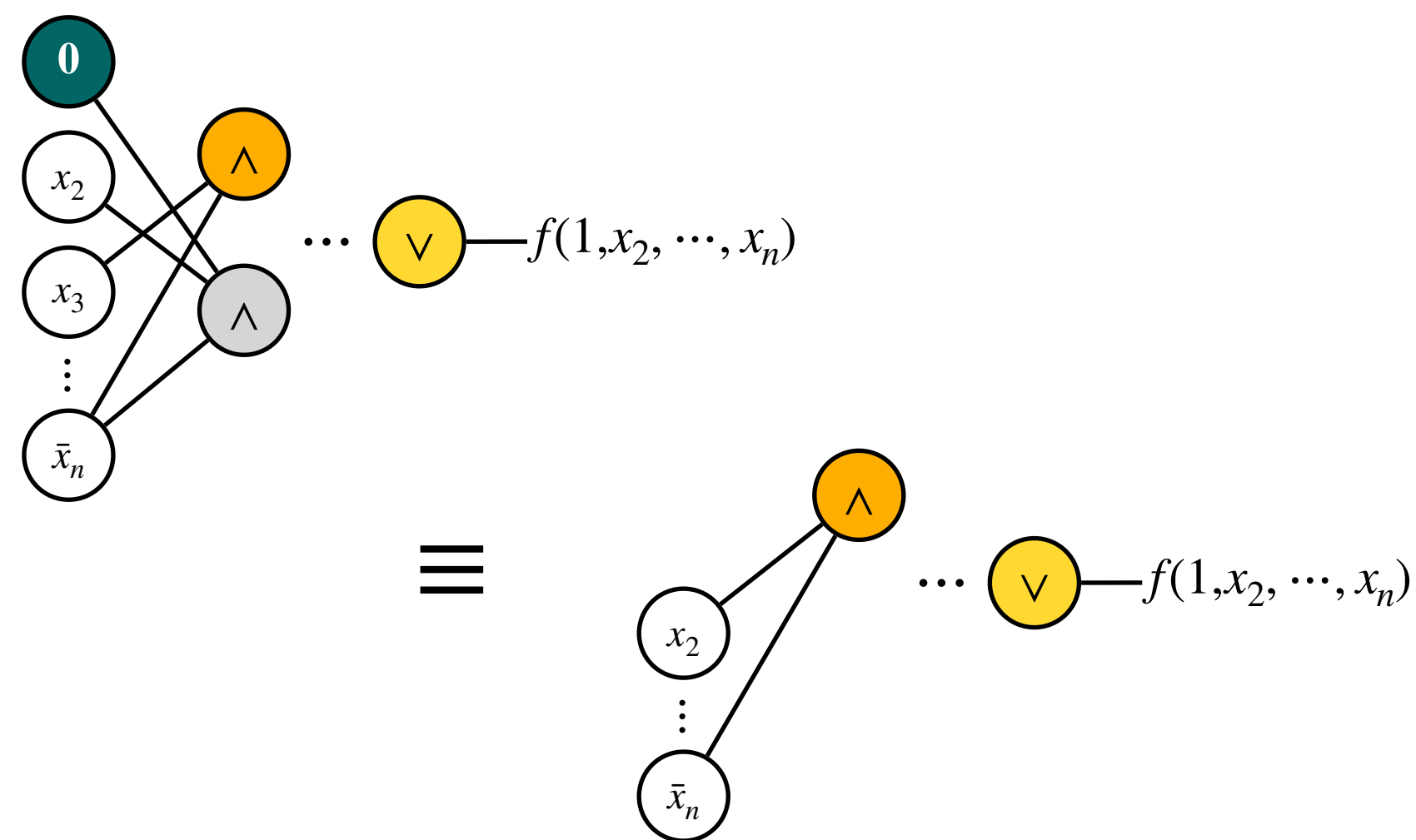
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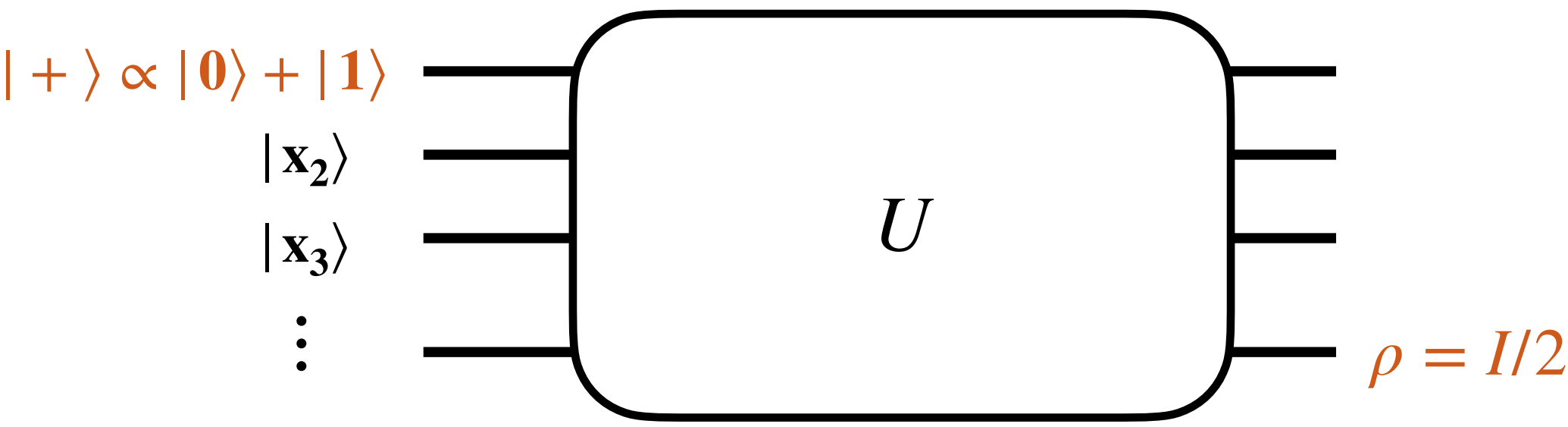
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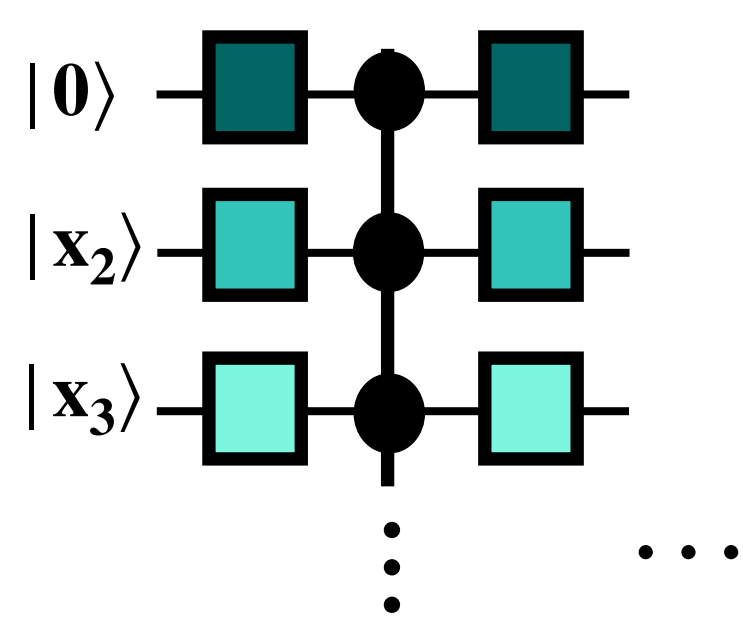
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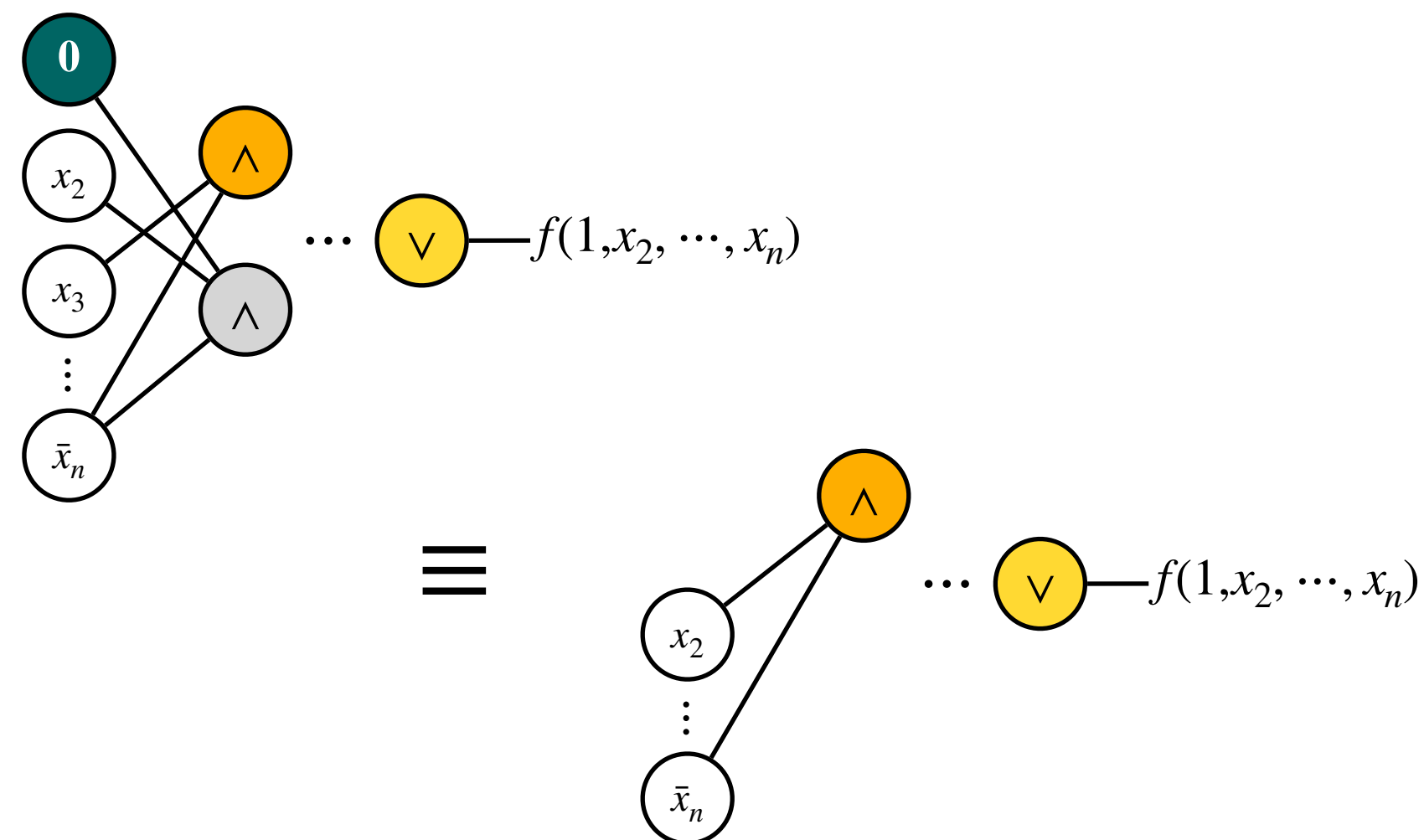
Subspace orthogonal to $|\vec{\theta}\rangle_S$?

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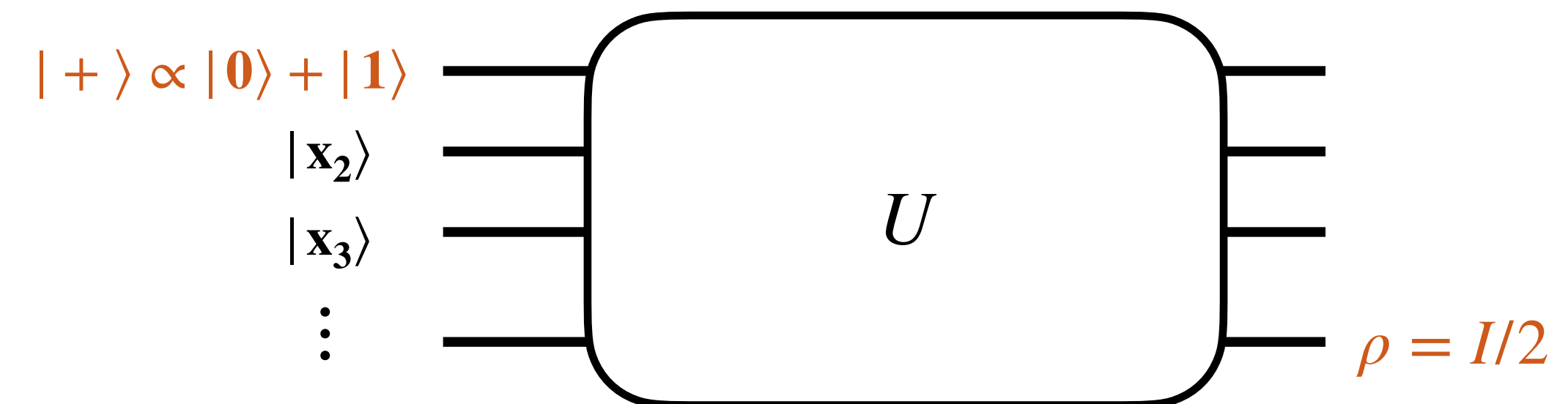
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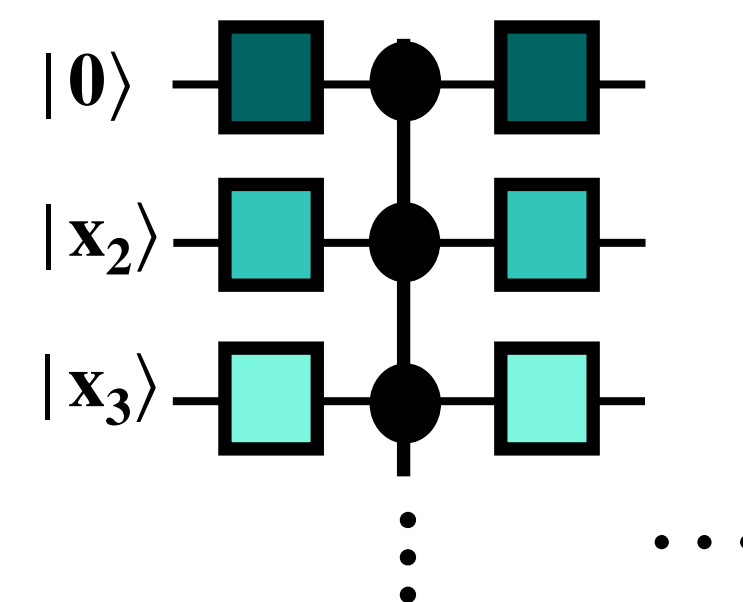
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Restrict to two qubit state to preserve parity and simplify circuit!

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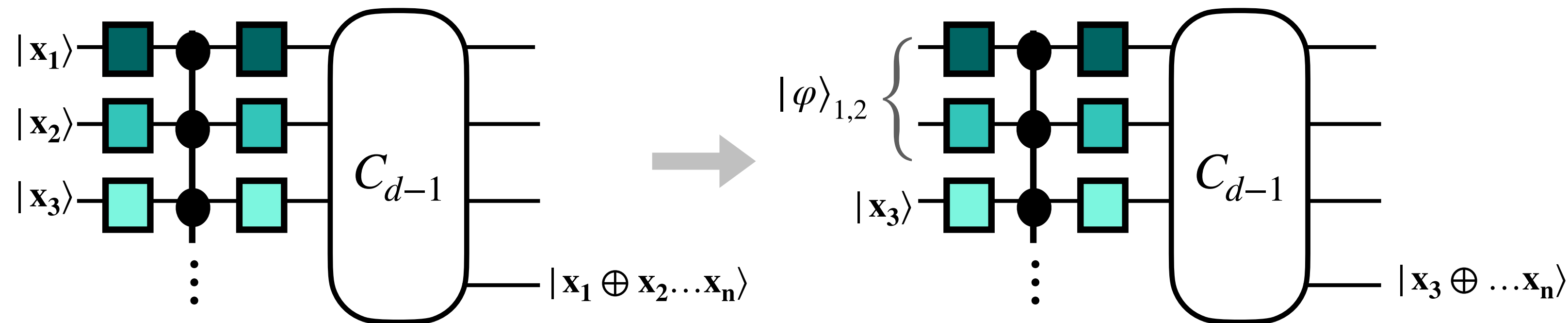
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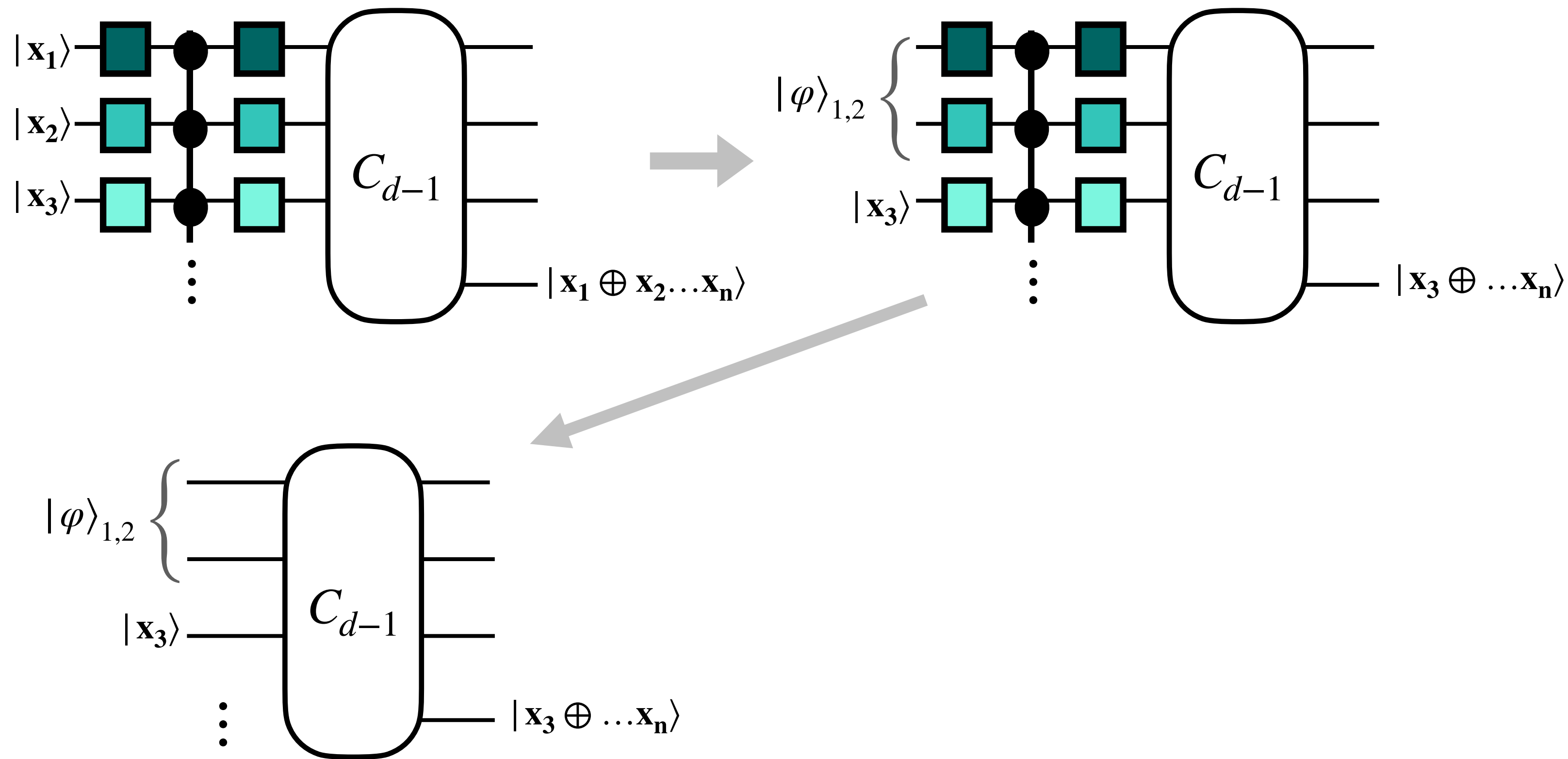


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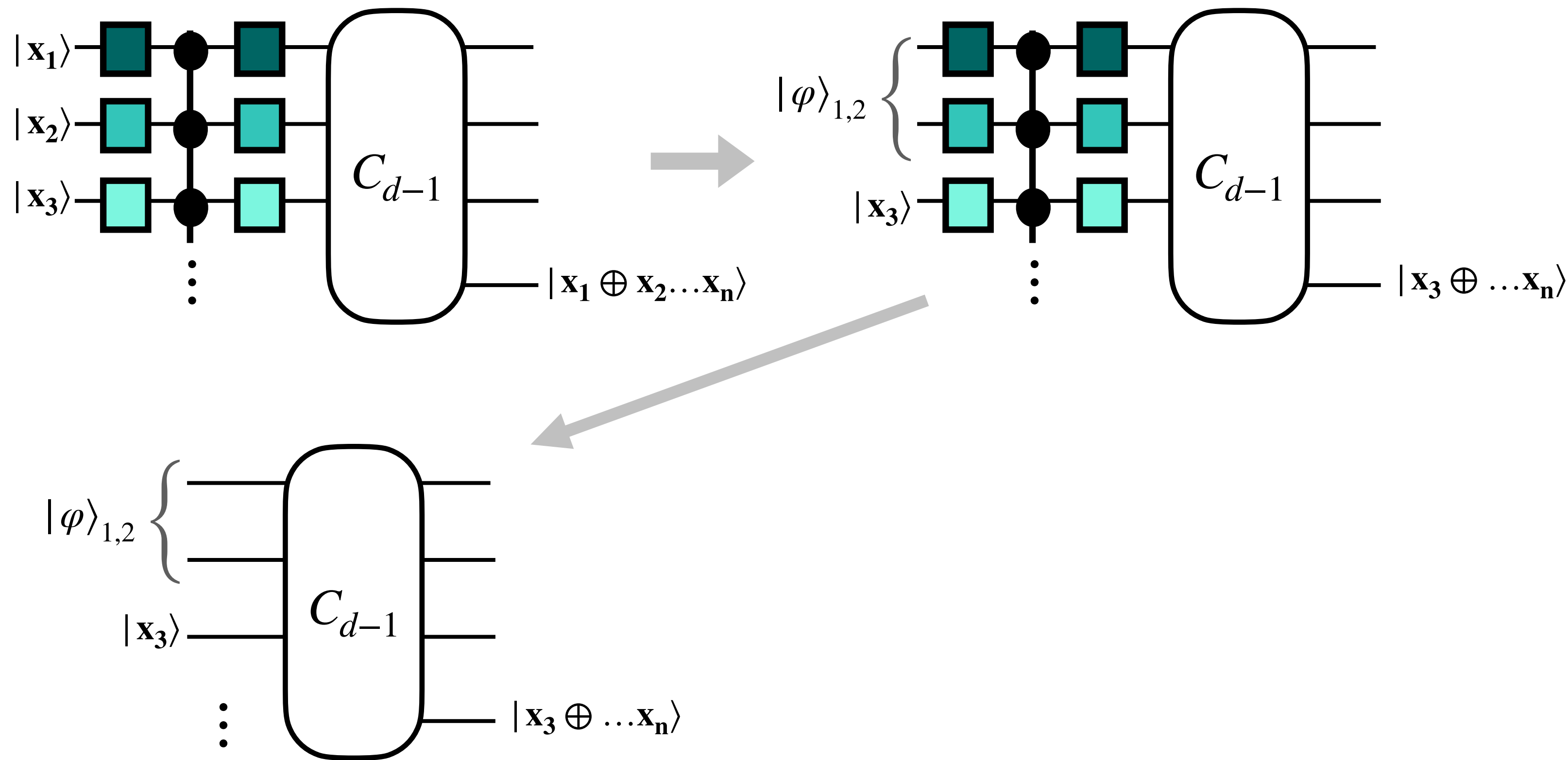


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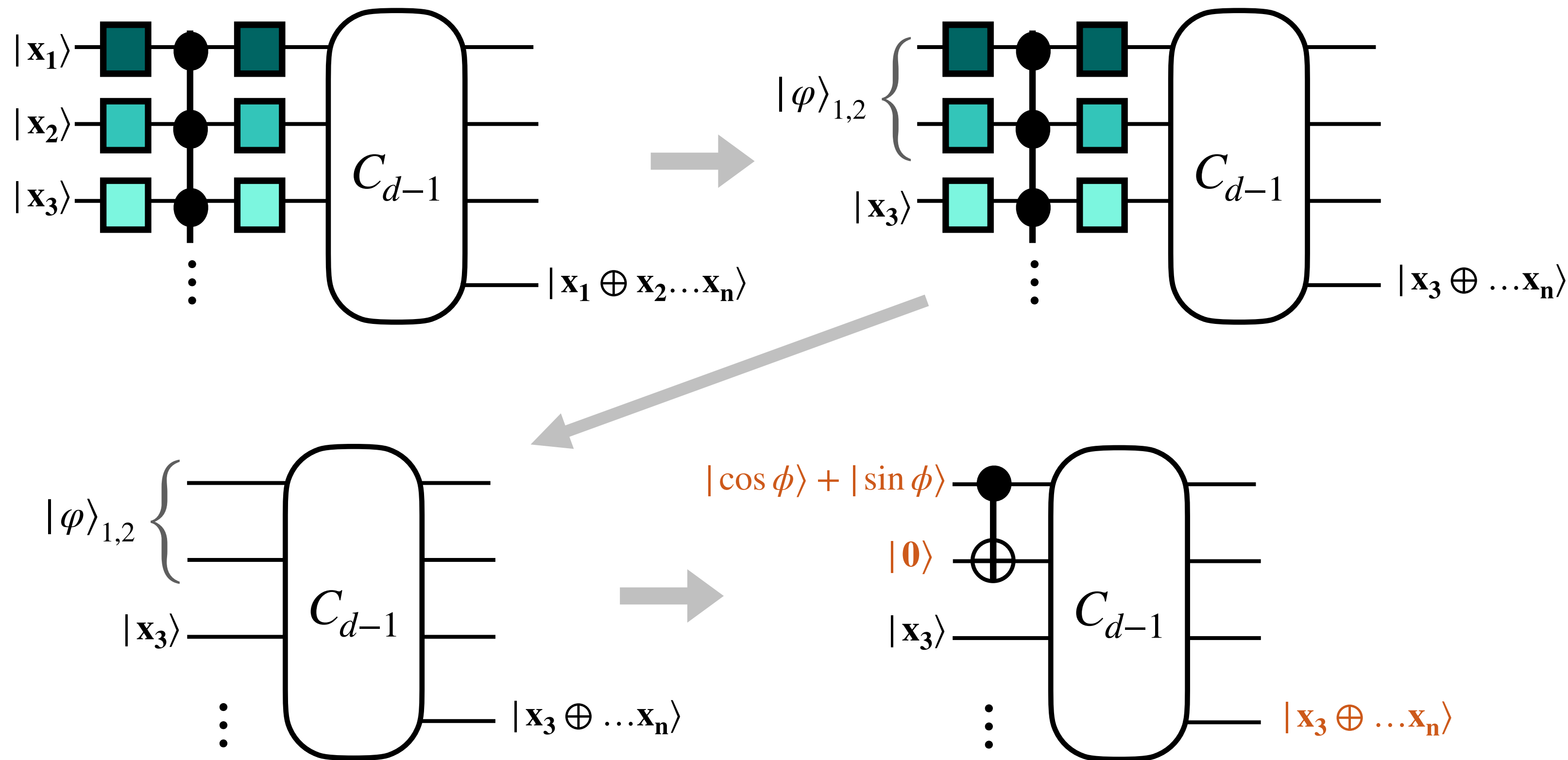
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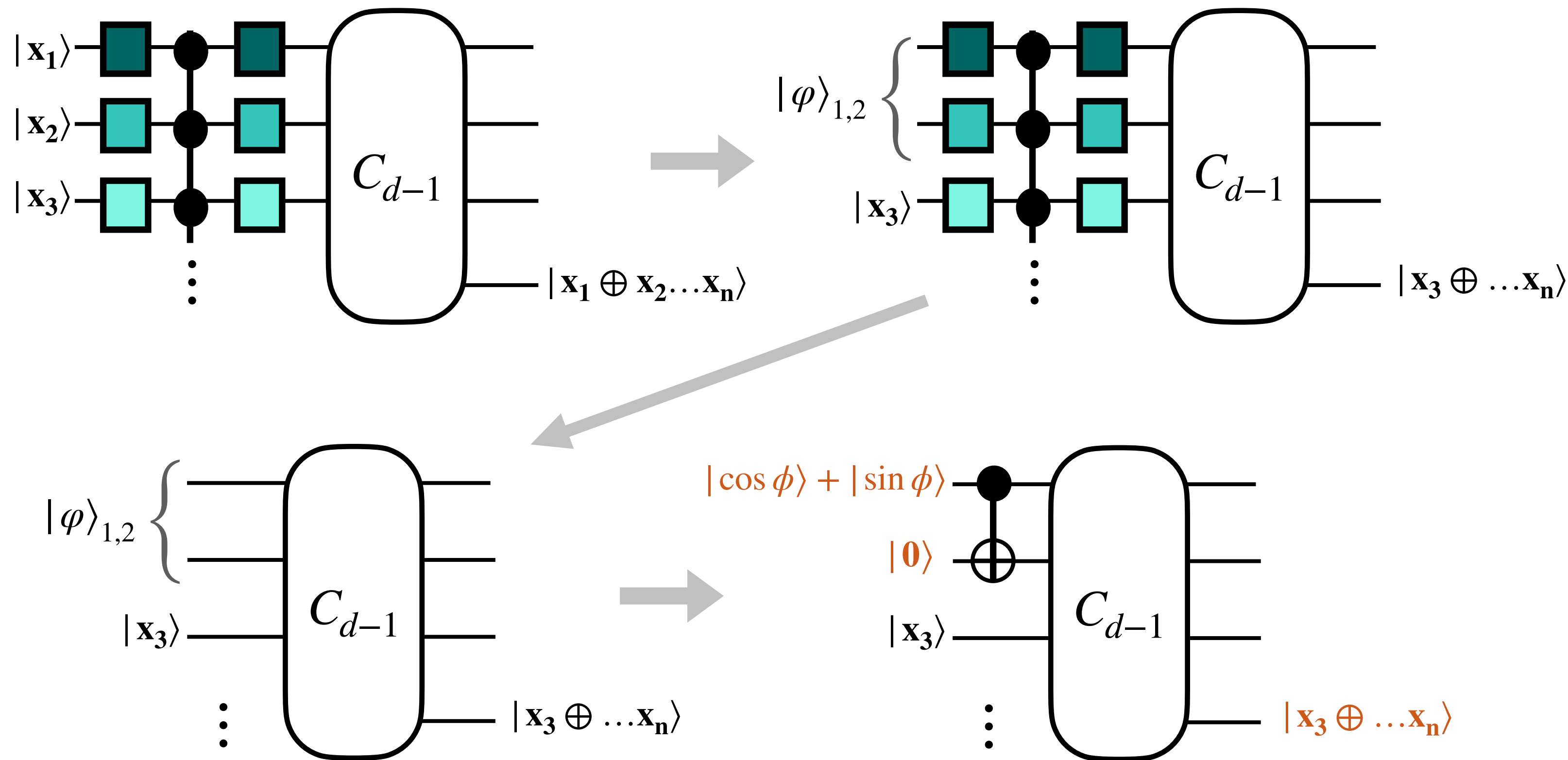
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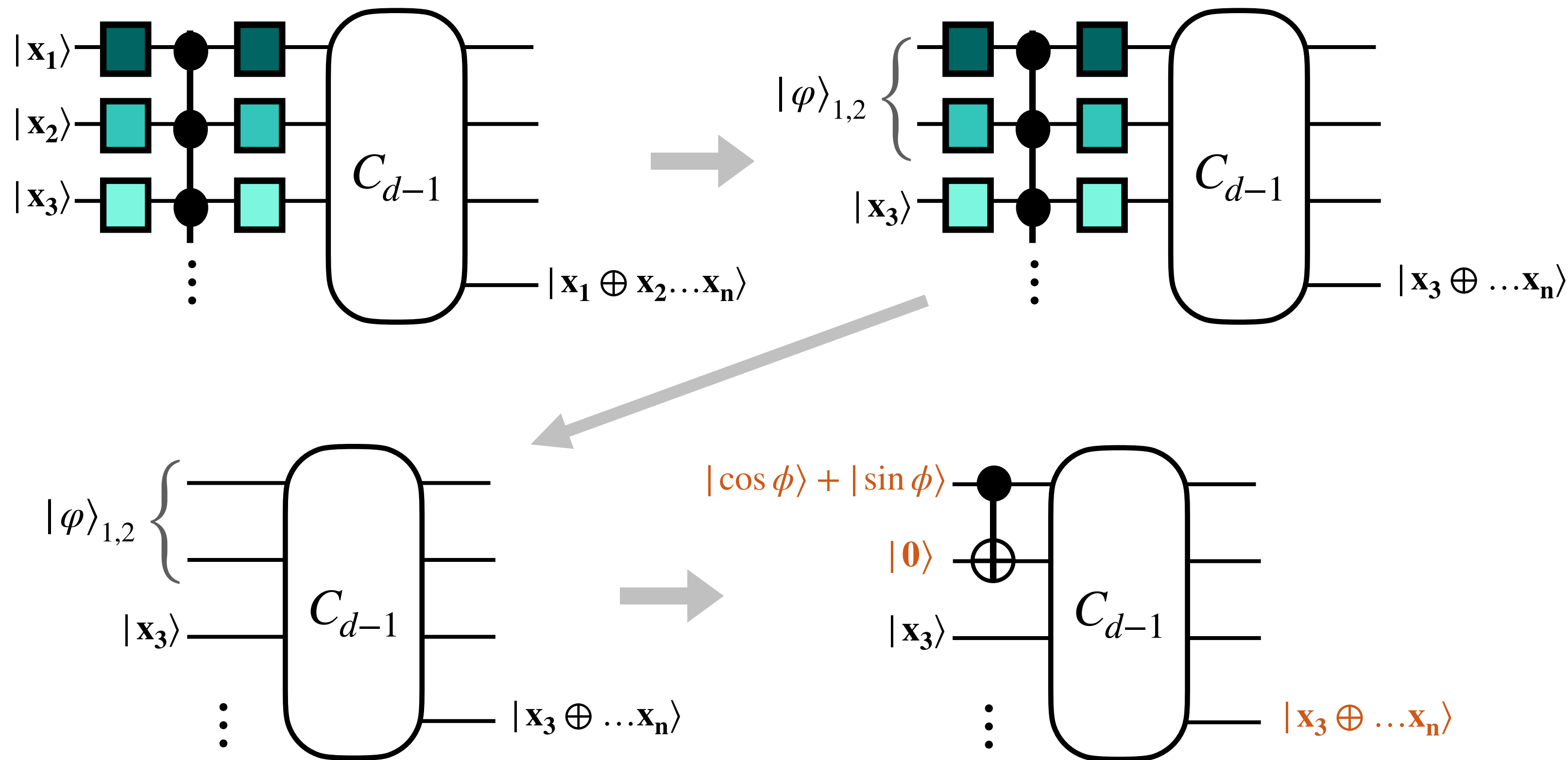
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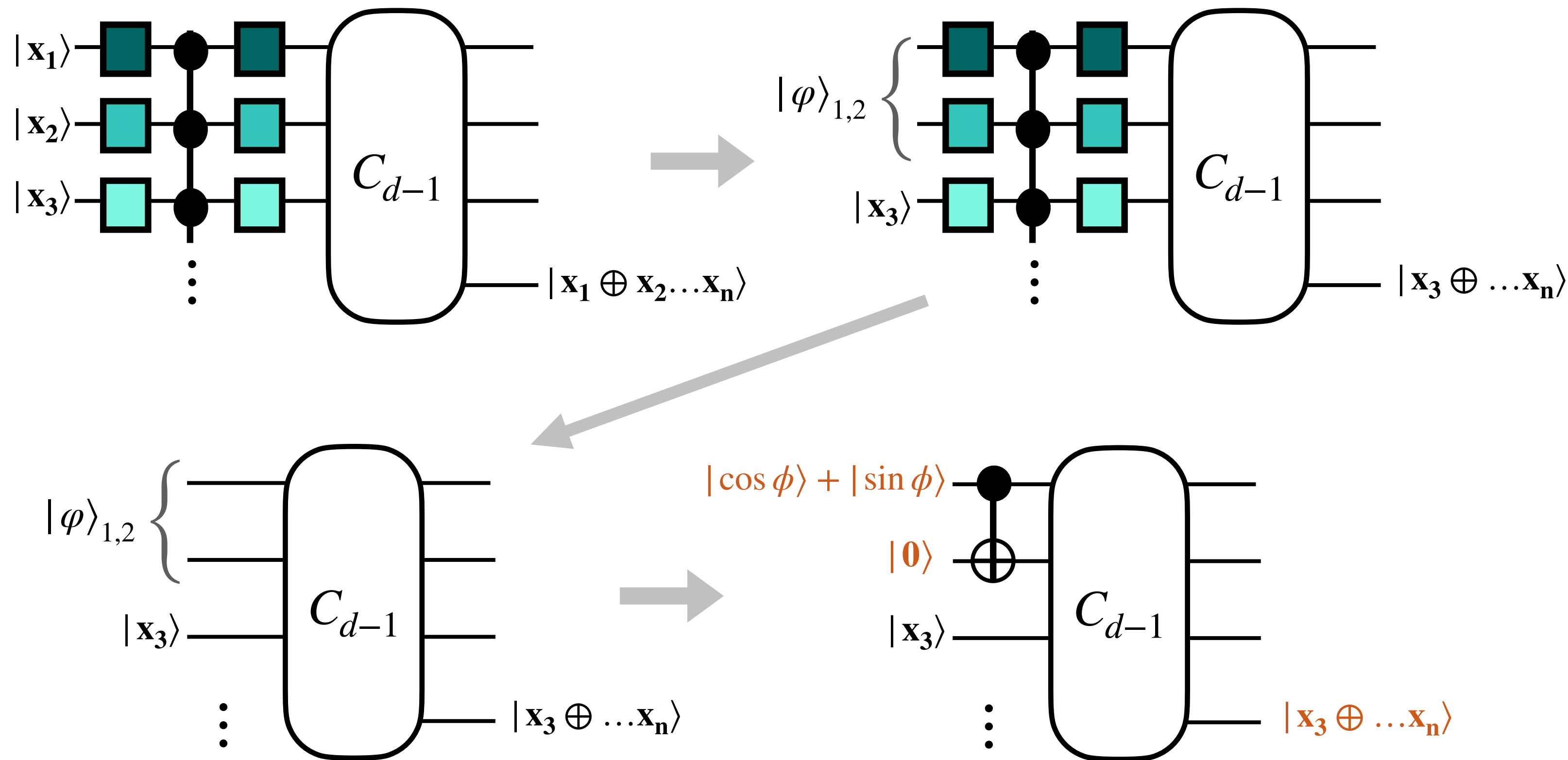
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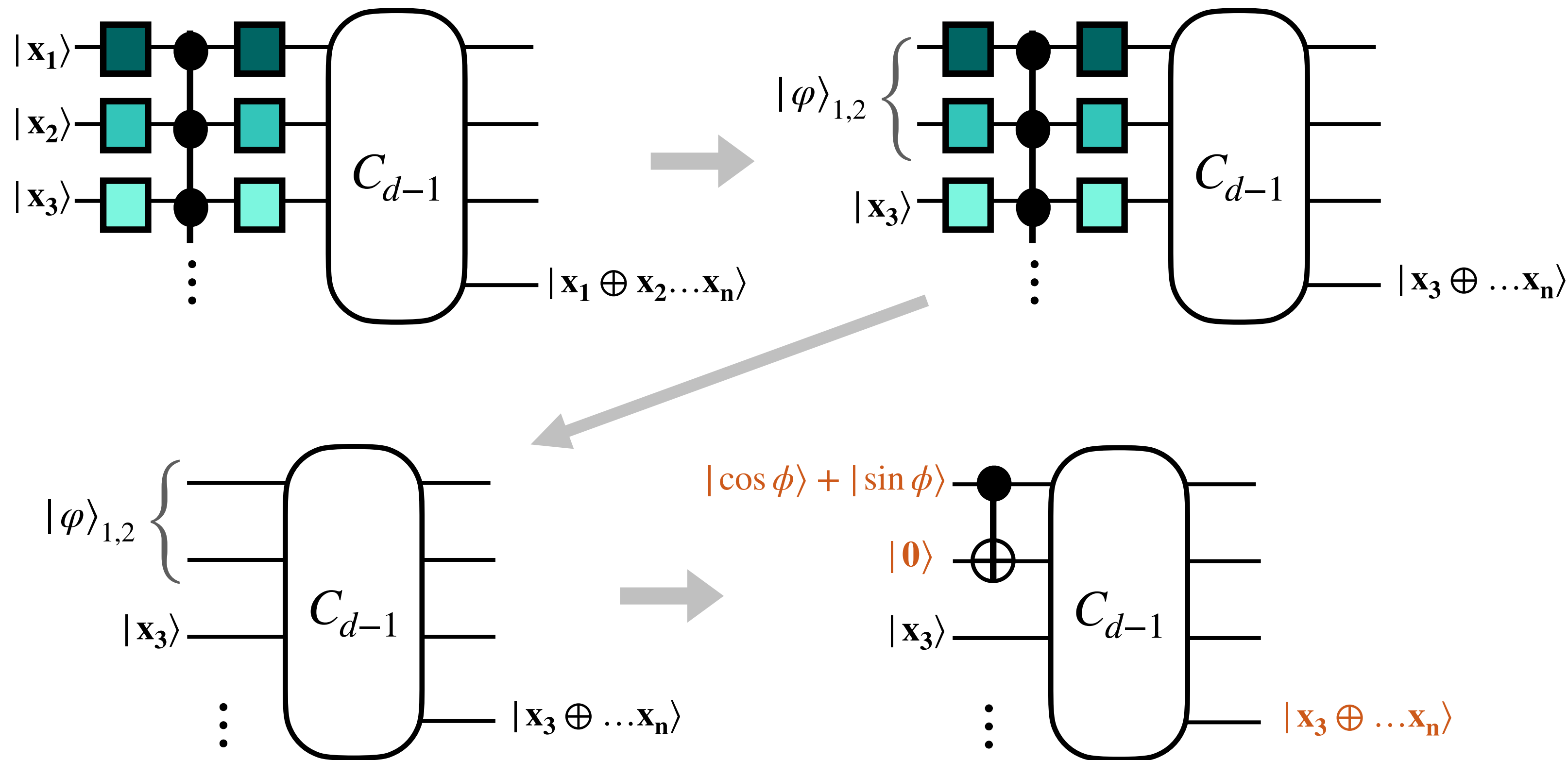
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- $\Rightarrow \geq n/3$ inputs preserved
depth $\leq d$, size $\leq s$ ■

Classical simulation at higher depths?

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Thank you